Fernanda Lima Kastensmidt Luigi Carro Ricardo Reis

Fault-Tolerance Techniques for SRAM-based FPGAs



L Cohen

Fault-Tolerance Techniques for SRAM-Based FPGAs Fernanda Lima Kastensmidt, Ricardo Reis, 2007-02-01 Fault tolerance in integrated circuits is not an exclusive concern regarding space designers or highly reliable application engineers Rather designers of next generation products must cope with reduced margin noises due to technological advances The continuous evolution of the fabrication technology process of semiconductor components in terms of transistor geometry shrinking power supply speed and logic density has significantly reduced the reliability of very deep submicron integrated circuits in face of the various internal and external sources of noise The very popular Field Programmable Gate Arrays customizable by SRAM cells are a consequence of the integrated circuit evolution with millions of memory cells to implement the logic embedded memories routing and more recently with embedded microprocessors cores. These re programmable systems on chip platforms must be fault tolerant to cope with present days requirements This book discusses fault tolerance techniques for SRAM based Field Programmable Gate Arrays FPGAs It starts by showing the model of the problem and the upset effects in the programmable architecture In the sequence it shows the main fault tolerance techniques used nowadays to protect integrated circuits against errors A large set of methods for designing fault tolerance systems in SRAM based FPGAs is described Some presented techniques are based on developing a new fault tolerant architecture with new robustness FPGA elements Other techniques are based on protecting the high level hardware description before the synthesis in the FPGA The reader has the flexibility of choosing the most suitable fault tolerance technique for its project and to compare a set of fault tolerant techniques for programmable logic applications FPGAs and Parallel Architectures for Aerospace Applications Fernanda Kastensmidt, Paolo Rech, 2015-12-07 This book introduces the concepts of soft errors in FPGAs as well as the motivation for using commercial off the shelf COTS FPGAs in mission critical and remote applications such as aerospace The authors describe the effects of radiation in FPGAs present a large set of soft error mitigation techniques that can be applied in these circuits as well as methods for qualifying these circuits under radiation Coverage includes radiation effects in FPGAs fault tolerant techniques for FPGAs use of COTS FPGAs in aerospace applications experimental data of FPGAs under radiation FPGA embedded processors under radiation and fault injection in FPGAs Since dedicated parallel processing architectures such as GPUs have become more desirable in aerospace applications due to high computational power GPU analysis under radiation is also discussed **Embedded Systems** Krzysztof Iniewski, 2012-10-26 Covers the significant embedded computing technologies highlighting their applications in wireless communication and computing power An embedded system is a computer system designed for specific control functions within a larger system often with real time computing constraints It is embedded as part of a complete device often including hardware and mechanical parts Presented in three parts Embedded Systems Hardware Design and Implementation provides readers with an immersive introduction to this rapidly growing segment of the computer industry Acknowledging the fact that embedded

systems control many of today s most common devices such as smart phones PC tablets as well as hardware embedded in cars TVs and even refrigerators and heating systems the book starts with a basic introduction to embedded computing systems It hones in on system on a chip SoC multiprocessor system on chip MPSoC and network on chip NoC It then covers on chip integration of software and custom hardware accelerators as well as fabric flexibility custom architectures and the multiple I O standards that facilitate PCB integration Next it focuses on the technologies associated with embedded computing systems going over the basics of field programmable gate array FPGA digital signal processing DSP and application specific integrated circuit ASIC technology architectural support for on chip integration of custom accelerators with processors and O S support for these systems Finally it offers full details on architecture testability and computer aided design CAD support for embedded systems soft processors heterogeneous resources and on chip storage before concluding with coverage of software support in particular O S Linux Embedded Systems Hardware Design and Implementation is an ideal book for design engineers looking to optimize and reduce the size and cost of embedded system products and increase their reliability and performance Biologically-Inspired Collaborative Computing Mike Hinchey, Anastasia Pagnoni, Franz J. Rammig, Hartmut Schmeck, Look deep into nature and you will understand everything better advised Albert Einstein In recent years the research communities in Computer Science Engineering and other disciplines have taken this message to heart and a relatively new field of biologically inspired computing has been born Inspiration is being drawn from nature from the behaviors of colonies of ants of swarms of bees and even the human body This new paradigm in computing takes many simple autonomous objects or agents and lets them jointly perform a complex task without having the need for centralized control In this paradigm these simple objects interact locally with their environment using simple rules Applications include optimization algorithms communications networks scheduling and decision making supply chain management and robotics to name just a few There are many disciplines involved in making such systems work from artificial intelligence to energy aware systems Often these disciplines have their own field of focus have their own conferences or only deal with specialized s problems e g swarm intelligence biologically inspired computation sensor networks The Second IFIP Conference on Biologically Inspired Collaborative Computing aims to bridge this separation of the scientific community and bring together researchers in the fields of Organic Computing Autonomic Computing Self Organizing Systems Pervasive Computing and related areas We are very pleased to have two very important keynote presentations Swarm Robotics The Coordination of Robots via Swarm Intelligence Principles by Marco Dorigo Universit Libre de Bruxelles Belgium of which an **CMOS SRAM Circuit Design and Parametric Test in Nano-Scaled Technologies** abstract is included in this volume Andrei Pavlov, Manoj Sachdev, 2008-06-01 The monograph will be dedicated to SRAM memory design and test issues in nano scaled technologies by adapting the cell design and chip design considerations to the growing process variations with associated test issues Purpose provide process aware solutions for SRAM design and test challenges Digital Timing

Measurements Wolfgang Maichen, 2006-10-03 As many circuits and applications now enter the Gigahertz frequency range accurate digital timing measurements have become crucial in the design verification characterization and application of electronic circuits To be successful in this endeavour an engineer needs a knowledge base covering instrumentation measurement techniques signal integrity jitter and timing concepts and statistics Very often even the most experienced digital test engineers while mastering some of those subjects lack systematic knowledge or experience in the high speed signal area Digital Timing Measurements gives a compact practice oriented overview on all those subjects The emphasis is on useable concepts and real life guidelines that can be readily put into practice with references to the underlying mathematical theory It unites in one place a variety of information relevant to high speed testing measurement signal fidelity and instrumentation Design and Test Technology for Dependable Systems-on-Chip Ubar, Raimund, Raik, Jaan, Vierhaus, Heinrich Theodor, 2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC On-Chip Current Sensors for Reliable, Secure, and Low-Power Integrated Circuits Rodrigo Possamai Bastos, Frank Sill Torres, 2019-09-30 This book provides readers with insight into an alternative approach for enhancing the reliability security and low power features of integrated circuit designs related to transient faults hardware Trojans and power consumption The authors explain how the addition of integrated sensors enables the detection of ionizing particles and how this information can be processed at a high layer The discussion also includes a variety of applications such as the detection of hardware Trojans and fault attacks and how sensors can operate to provide different body bias levels and reduce power costs Readers can benefit from these sensors based approaches through designs with fast response time non intrusive integration on gate level and reasonable design costs Oscillation-Based Test in Mixed-Signal Circuits Gloria Huertas Sánchez, Diego Vázquez García de la Vega, Adoración Rueda Rueda, Jose Luis Huertas Díaz, 2007-06-03 This book presents the development and experimental validation of the structural test strategy called Oscillation Based Test OBT in short The results presented here assert not only from a theoretical point of view but also based on a wide experimental support that OBT is an efficient defect oriented test solution complementing the existing functional test techniques for mixed signal circuits Defect-Oriented Testing for Nano-Metric CMOS VLSI Circuits Manoj Sachdev, José Pineda de Gyvez, 2007-06-04 The 2nd edition of defect oriented testing has been extensively updated New chapters on Functional Parametric Defect Models and Inductive fault Analysis and Yield Engineering have been added to provide a link between defect sources and yield The chapter on RAM testing has been updated with focus on parametric and SRAM stability testing Similarly newer material has been incorporated in digital fault modeling and analog testing chapters The strength of Defect Oriented Testing for nano Metric CMOS VLSIs lies in its industrial relevance The Core Test Wrapper Handbook Francisco da Silva, Teresa McLaurin, Tom Waayers, 2006-09-15 In the early to mid 1990 s while working

at what was then Motorola Se conductor business changes forced my multi hundred dollar microprocessor to become a tens of dollars embedded core I ran into first hand the problem of trying to deliver what used to be a whole chip with something on the order of over 400 interconnect signals to a design team that was going to stuff it into a package with less than 220 signal pins and surround it with other logic I also ran into the problem of delivering microprocessor specification verifition a microprocessor is not just about the functions and instructions included with the instruction set but also the MIPs rating at some given f quency I faced two dilemmas one I could not deliver functional vectors without significant development of off core logic to deal with the reduced chip I O map and everybody s I O map was going to be a little different and two the JTAG 1149 1 boundary scan ring that was around my core when it was a chip was going to be woefully inadequate since it did not support speed signal application and capture and independent use separate from my core I considered the problem at length and came up with my own solution that was predominantly a separate non JTAG scan test wrapper that supported at speed application of launch capture cycles using the system clock But my problems weren t over at that point either Nanotechnologies Mohammad Tehranipoor, 2007-12-08 Emerging Nanotechnologies Test Defect Tolerance and Reliability covers various technologies that have been developing over the last decades such as chemically assembled electronic nanotechnology Quantum dot Cellular Automata QCA and nanowires and carbon nanotubes Each of these technologies offers various advantages and disadvantages Some suffer from high power some work in very low temperatures and some others need indeterministic bottom up assembly These emerging technologies are not considered as a direct replacement for CMOS technology and may require a completely new architecture to achieve their functionality Emerging Nanotechnologies Test Defect Tolerance and Reliability brings all of these issues together in one place for readers and researchers who are interested in this rapidly changing field New Methods of Concurrent Checking Michael Gössel, Vitaly Ocheretny, Egor Sogomonyan, Daniel Marienfeld, 2008-04-26 Computers are everywhere around us We for example as air passengers car drivers laptop users with Internet connection cell phone owners hospital patients inhabitants in the vicinity of a nuclear power station students in a digital library or customers in a supermarket are dependent on their correct operation Computers are incredibly fast inexpensive and equipped with almost unimag able large storage capacity Up to 100 million transistors per chip are quite common today a single transistor for each citizen of a large capital city in the world can be 2 easily accommodated on an ordinary chip The size of such a chip is less than 1 cm This is a fantastic achievement for an unbelievably low price However the very small and rapidly decreasing dimensions of the transistors and their connections over the years are also the reason for growing problems with reliability that will dramatically increase for the nano technologies in the near future Can we always trust computers Are computers always reliable Are chips sufficiently tested with respect to all possible permanent faults if we buy them at a low price or have errors due to undetected permanent faults to be discovered by c current checking Besides permanent faults many temporary or transient faults are also to be expected

Reconfigurable Computing: Architectures, Tools, and Applications Diana Goehringer, Marco Domenico Santambrogio, João M.P. Cardoso, Koen Bertels, 2014-04-09 This book constitutes the thoroughly refereed conference proceedings of the 10th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2014 held in Vilamoura Portugal in April 2014 The 16 revised full papers presented together with 17 short papers and 6 special session papers were carefully reviewed and selected from 57 submissions The topics covered are applications methods frameworks and OS for debug over clocking and relocation memory architectures methodologies and tools and architectures **Proceedings**, 2006

Nanotechnology: Concepts, Methodologies, Tools, and Applications Management **SBCCI 2006** ,2006 Association, Information Resources, 2014-02-28 Over the past few decades devices and technologies have been significantly miniaturized from one generation to the next providing far more potential in a much smaller package The smallest of these recently developed tools are miniscule enough to be invisible to the naked eye Nanotechnology Concepts Methodologies Tools and Applications describes some of the latest advances in microscopic technologies in fields as diverse as biochemistry materials science medicine and electronics Through its investigation of theories applications and new developments in the nanotechnology field this impressive reference source will serve as a valuable tool for researchers engineers academics and The British National Bibliography Arthur James Wells, 2006 **Index to IEEE Publications** Institute of students alike Electrical and Electronics Engineers, 1994 Fault-Tolerance Techniques for Spacecraft Control Computers Mengfei Yang, Gengxin Hua, Yanjun Feng, Jian Gong, 2017-01-23 Comprehensive coverage of all aspects of space application oriented fault tolerance techniques Experienced expert author working on fault tolerance for Chinese space program for almost three decades Initiatively provides a systematic texts for the cutting edge fault tolerance techniques in spacecraft control computer with emphasis on practical engineering knowledge Presents fundamental and advanced theories and technologies in a logical and easy to understand manner Beneficial to readers inside and outside the area of space applications

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