

The background of the book cover is a high-magnification, false-color aerial photograph of a complex integrated circuit (IC) die. The die is rectangular and filled with a dense, intricate pattern of circuitry. Various functional blocks are highlighted in different colors: large rectangular areas in bright green and red, and a prominent diagonal strip in purple and blue. A network of fine, dark lines represents the interconnects between these blocks. The overall image has a technical, scientific feel, emphasizing the complexity and precision of modern semiconductor technology.

FOURTH EDITION

CMOS VLSI DESIGN

**A CIRCUITS
AND
SYSTEMS
PERSPECTIVE**

NEIL H. E. WESTE

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Cmos Vlsi Design By Weste And Harris Solution Manual

James B. Kuo, Jea-Hong Lou



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