

The background of the cover is a high-magnification, false-color micrograph of a silicon microchip die. The die is rectangular and filled with a complex, grid-like pattern of circuitry. Different functional blocks are highlighted in various colors: large rectangular areas in red, green, and blue, and smaller, more intricate patterns in purple, yellow, and orange. The overall appearance is that of a highly detailed, multi-layered electronic circuit.

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# Cmos Vlsi Design Solution Manual

**Roman Wölfel**



## **Cmos Vlsi Design Solution Manual:**

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