EVGA — ELEET X1

Z790 CLASSIFIED

SETUP

| CPU ▼ |
| Clock ▲ |

| Overclocking | Memory | Monitoring | System | LED |

Package | Per-Core

Profile

Core Ratio

Multiplier   x49 (4 - 51)
BCLK   100.00 MHz
P Core #1   4900.00 MHz , 1.239 V , 32 °C
P Core #2   4900.00 MHz , 1.239 V , 31 °C
P Core #3   4900.00 MHz , 1.239 V , 33 °C
P Core #4   4900.00 MHz , 1.239 V , 32 °C
P Core #5   4900.00 MHz , 1.239 V , 33 °C
P Core #6   4900.00 MHz , 1.239 V , 30 °C
P Core #7   4900.00 MHz , 1.239 V , 36 °C
P Core #8   4900.00 MHz , 1.239 V , 31 °C
E Core #1   3700.00 MHz , 1.239 V , 35 °C
E Core #2   3700.00 MHz , 1.239 V , 35 °C
E Core #3   3700.00 MHz , 1.239 V , 35 °C
E Core #4   3700.00 MHz , 1.239 V , 35 °C
E Core #5   3700.00 MHz , 1.239 V , 32 °C
E Core #6   3700.00 MHz , 1.239 V , 32 °C

| Memory ▼ |

P Core #1   x52
P Core #2   x51
P Core #3   x50
P Core #4   x50
P Core #5   x49
P Core #6   x49
P Core #7   x49
P Core #8   x49
E Core #1   x39
E Core #2   x39
E Core #3   x39
E Core #4   x39
E Core #5   x37
E Core #6   x37
E Core #7   x37
E Core #8   x37
Ring   x36

Current Limit Throttling

Clock

v1.0.13.6

| Load | Default | Apply | Save |

Evga Support User Guide

RJ Shavelson

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
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web mason building 3 constructor 2 3 course structure table below depicts the distribution of training hours across various course elements during a period of one year s no course element notional training hours 1 professional skill trade practical 1000 2 professional knowledge trade theory 280 3 workshop calculation science 8 0