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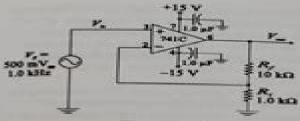


Figure 20-3

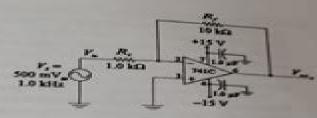


Figure 20-4

In this step you will test an inverting amplifier. All data are to be recorded in Table 20-2. The circuit is illustrated in Figure 20-4. The closed-loop gain is:

$$A_{cl(0)} = -\frac{R_f}{R_i}$$

- (a) Use the same resistors for  $R_i$  and  $R_i$  as in step 1. Record the measured value of resistance in Table 20-2.
- (b) Using the measured resistance, compute and record the closed-loop gain of the inverting amplifier.
- (c) Calculate V using the computed closed-loop gain.
- (d) Connect the circuit shown in Figure 20-4. Set the signal generator for a 500 mV<sub>se</sub> sine wave at 1 kHz. The generator should have no dc offset.
- (e) Measure and record the output voltage, V\_\_\_
- (f) Measure and record the voltage at pin 2. This point is called a virtual ground because of the effect of negative feedback.
- (g) Place a 1.0 k $\Omega$  test resistor in series with the generator and R. Measure the input resistance of the circuit,  $R_{in}$  based on the voltage drop across the test resistor. In this case, you should observe that the drop across the test resistor is about the same as the drop across R.

Table 20-2 \*

R, Measured	R, Meanwood	V <sub>in</sub> Measured	A.en Computed	V <sub>res</sub> Computed Measured	V <sub>c.</sub> , Measured (pm 2)	R <sub>in</sub> Measured
		500 mV <sub>m</sub>	- 100 3			

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David M Buchla, Floyd

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