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# Chip Design for Submicron VLSI:

## CMOS Layout and Simulation

John P. Uyemura



This book is written for use in a  
VLSI Design course. It covers all  
aspects of VLSI design, from  
system level to fabrication.  
It is suitable for both  
undergraduate and graduate  
students.

# Chip Design For Submicron Vlsi Cmos Layout And Simulation

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