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29th IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2021 Singapore, October 4–8, 2021 Revised and Extended Selected Papers



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VLSI-SoC: Technologies for Systems Integration Jürgen Becker, Marcelo De Oliveira Johann, Ricardo Reis, 2013-11-27 This book contains extended and revised versions of the best papers presented at the 17th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2009 held in Florian polis Brazil in October 2009 The 8 papers included in the book together with two keynote talks were carefully reviewed and selected from 27 papers presented at the conference The papers cover a wide variety of excellence in VLSI technology and advanced research addressing the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges VLSI-SoC: New Technology Enabler both at the physical and system design levels as well as in the test of theses systems Carolina Metzler, Pierre-Emmanuel Gaillardon, Giovanni De Micheli, Carlos Silva-Cardenas, Ricardo Reis, 2020-07-22 This book contains extended and revised versions of the best papers presented at the 27th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2019 held in Cusco Peru in October 2019 The 15 full papers included in this volume were carefully reviewed and selected from the 28 papers out of 82 submissions presented at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like heterogeneous neuromorphic and brain inspired biologically inspired approximate computing systems VLSI-SoC: Design Trends Andrea Calimera, Pierre-Emmanuel Gaillardon, Kunal Korgaonkar, Shahar Kvatinsky, Ricardo Reis, 2021-07-14 This book contains extended and revised versions of the best papers presented at the 28th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2020 held in Salt Lake City UT USA in October 2020 The 16 full papers included in this volume were carefully reviewed and selected from the 38 papers out of 74 submissions presented at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like low power design of RF analog and mixed signal circuits EDA tools for the synthesis and verification of heterogenous

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stimulating new challenges both at the physical and system design levels as well as in the test of theses systems VLSI-SoC: Research Trends in VLSI and Systems on Chip Giovanni De Micheli, Salvador Mir, Ricardo Reis, 2010-08-23 This book contains extended and revised versions of the best papers presented during the fourteenth IFIP TC 10 WG 10 5 International Conference on Very Large Scale Integration This conference provides a forum to exchange ideas and show industrial and academic research results in microelectronics design. The current trend toward increasing chip integration and technology process advancements brings about stimulating new challenges both at the physical and system VLSI-SoC Jürgen Becker, 2011 Very Large Scale Integration (VLSI-SoC), 2014 22nd International design levels VLSI-SoC: Design Trends Andrea Calimera, Pierre-Emmanuel Gaillardon, Kunal Conference on ,2014 Korgaonkar, Shahar Kvatinsky, Ricardo Reis, 2021 This book contains extended and revised versions of the best papers presented at the 28th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2020 held in Salt Lake City UT USA in October 2020 The 16 full papers included in this volume were carefully reviewed and selected from the 38 papers out of 74 submissions presented at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like low power design of RF analog and mixed signal circuits EDA tools for the synthesis and verification of heterogenous SoCs accelerators for cryptography and deep learning and on chip Interconnection system reliability and testing and integration of 3D ICs The conference was held virtually **VLSI-Soc** Andreas Burg, Avse Coskun, Matthew VLSI-SoC: Technology Advancement on SoC Design Victor Grimblatt, Chip-Hong Chang, Ricardo Guthaus, 2013-11-30 Reis, Anupam Chattopadhyay, Andrea Calimera, 2022 This book contains extended and revised versions of the best papers presented at the 29th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2021 held in Singapore in October 2021 The 12 full papers included in this volume were carefully reviewed and selected from the 44 papers out of 75 submissions presented at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like low power design of RF analog and mixed signal circuits EDA tools for the synthesis and verification of heterogenous SoCs accelerators for cryptography and deep learning and on chip Interconnection system reliability and testing and integration of 3D ICs The conference was held virtually 2014 22nd International Conference on Very Large Scale Integration (VLSI-SoC) Lorena Garcia, 2014

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