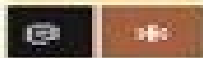




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COMPREHENSIVE FUNCTIONAL VERIFICATION

THE
COMPLETE
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BRUCE WILE JOHN C. GOSS WOLFGANG ROESNER

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Algorithmic Learning Theory Yoav Freund, László Györfi, György Turán, Thomas Zeugmann, 2008-10-02 This volume contains papers presented at the 19th International Conference on Algorithmic Learning Theory ALT 2008 which was held in Budapest Hungary during October 13 16 2008 The conference was co located with the 11th ernational Conference on Discovery Science, DS 2008 The

technical program of ALT 2008 contained 31 papers selected from 46 submissions and 5 invited talks The invited talks were presented in joint sessions of both conferences ALT 2008 was the 19th in the ALT conference series established in Japan in 1990 The series Analogical and Inductive Inference is a predecessor of this series it was held in 1986 1989 and 1992 co located with ALT in 1994 and subsequently merged with ALT ALT maintains its strong connections to Japan but has also been held in other countries such as Australia Germany Italy Singapore Spain and the USA The ALT conference series is supervised by its Steering Committee Naoki Abe IBM TJ Watson Research Center

Post-Silicon and Runtime Verification for Modern Processors Ilya Wagner, Valeria Bertacco, 2010-11-25 The purpose of this book is to survey the state of the art and evolving directions in post silicon and runtime verification The authors start by giving an overview of the state of the art in verification particularly current post silicon methodologies in use in the industry both for the domain of processor pipeline design and for memory subsystems They then dive into the presentation of several new post silicon verification solutions aimed at boosting the verification coverage of modern processors dedicating several chapters to this topic The presentation of runtime verification solutions follows a similar approach This is an area of processor design that is still in its early stages of exploration and that holds the promise of accomplishing the ultimate goal of achieving complete correctness guarantees for microprocessor based computation The authors conclude the book with a look towards the future of late stage verification and its growing role in the processor life cycle

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