



# Mobile Technician App By FUJITSU GENERAL LIMITED



## Standard Error Codes (all indoor units from 2013)

Error Detail	Indoor LED Display Flashes			Wired RC	Error Detail	Indoor LED Display Flashes			Wired RC
	Operation	Timer	Economy			Operation	Timer	Economy	
Serial communication error	1	1	continuous	11	CU Discharge temp sensor error	7	1	continuous	71
Wired RC communication error	1	2	continuous	12	Compressor temp sensor error	7	2	continuous	72
Network communication error	1	4	continuous	14	CU Heat-Fx liquid temp sensor error	7	3	continuous	73
Check run unfinished	1	5	continuous	15	CU Ambient temp sensor error	7	4	continuous	74
Transmission/Peripheral connect error	1	6	continuous	16	CU Suction ref temp sensor error	7	5	continuous	75
Refrigerant circuit address setting error	2	1	continuous	21	2/3-way valve temp sensor error	7	6	continuous	76
Indoor unit capacity error	2	2	continuous	22	CU Heat sink temp sensor error	7	7	continuous	77
Combination error	2	3	continuous	23	Sub-Cool inlet/outlet temp sensor error	8	2	continuous	82
Connection unit number error	2	4	continuous	24	CU Liquid pipe temp sensor error	8	3	continuous	83
Address setting error	2	6	continuous	26	CU Current sensor error	8	4	continuous	84
Master/slave unit set-up error	2	7	continuous	27	HP/UP pressure sensor/switch error	8	6	continuous	86
IU connection error on WRC	2	9	continuous	29	CU Current trip detection	9	4	continuous	94
Power supply interruption error	3	1	continuous	31	Compressor rotor position error	9	5	continuous	95
Indoor PCB EEPROM error	3	2	continuous	32	CU fan motor error	9	7	continuous	97
Manual auto switch error	3	5	continuous	35	CU fan motor 2 error	9	8	continuous	98

# Fujitsu Fault Codes

**Israel Koren, C. Mani Krishna**



## **Fujitsu Fault Codes:**

Essentials of Error-Control Coding Techniques Hideki Imai, 2014-06-28 *Essentials of Error Control Coding Techniques* presents error control coding techniques with an emphasis on the most recent applications. It is written for engineers who use or build error control coding equipment. Many examples of practical applications are provided enabling the reader to obtain valuable expertise for the development of a wide range of error control coding systems. Necessary background knowledge of coding theory, the theory of error correcting codes, is also included so that the reader is able to assimilate the concepts and the techniques. The book is divided into two parts. The first provides the reader with the fundamental knowledge of the coding theory that is necessary to understand the material in the latter part. Topics covered include the principles of error detection and correction, block codes and convolutional codes. The second part is devoted to the practical applications of error control coding in various fields. It explains how to design cost effective error control coding systems. Many examples of actual error control coding systems are described and evaluated. This book is particularly suited for the engineer striving to master the practical applications of error control coding. It is also suitable for use as a graduate text for an advanced course in coding theory. **Scientific Information Bulletin**, 1992 **EDN**, 1987 *Architecture Design for Soft Errors* Shubu Mukherjee, 2011-08-29 *Architecture Design for Soft Errors* provides a comprehensive description of the architectural techniques to tackle the soft error problem. It covers the new methodologies for quantitative analysis of soft errors as well as novel cost effective architectural techniques to mitigate them. To provide readers with a better grasp of the broader problem definition and solution space, this book also delves into the physics of soft errors and reviews current circuit and software mitigation techniques. There are a number of different ways this book can be read or used in a course: as a complete course on architecture design for soft errors, covering the entire book; as a short course on architecture design for soft errors; and as a reference book on classical fault tolerant machines. This book is recommended for practitioners in semiconductor industry, researchers and developers in computer architecture, advanced graduate seminar courses on soft errors, and as a reference book for undergraduate courses in computer architecture. Helps readers build in fault tolerance to the billions of microchips produced each year, all of which are subject to soft errors. Shows readers how to quantify their soft error reliability. Provides state of the art techniques to protect against soft errors. Fault-Tolerant Systems Israel Koren, C. Mani Krishna, 2010-07-19 *Fault Tolerant Systems* is the first book on fault tolerance design with a systems approach to both hardware and software. No other text on the market takes this approach nor offers the comprehensive and up to date treatment that Koren and Krishna provide. This book incorporates case studies that highlight six different computer systems with fault tolerance techniques implemented in their design. A complete ancillary package is available to lecturers including online solutions manual for instructors and PowerPoint slides. Students, designers and architects of high performance processors will value this comprehensive overview of the field. The first book on fault tolerance design with a systems approach. Comprehensive

coverage of both hardware and software fault tolerance as well as information and time redundancy Incorporated case studies highlight six different computer systems with fault tolerance techniques implemented in their design Available to lecturers is a complete ancillary package including online solutions manual for instructors and PowerPoint slides

**Mini-micro Systems** ,1987-07 *Reliable Computer Systems* Daniel P. Siewiorek,Robert S. Swarz,1998-12-15 This classic reference work is a comprehensive guide to the design evaluation and use of reliable computer systems It includes case studies of reliable systems from manufacturers such as Tandem Stratus IBM and Digital It covers special systems such as the Galileo Orbiter fault protection system and AT T telephone switching system processors **Reliable Computer Systems** Daniel Siewiorek,Robert Swarz,2014-06-28 Enhance your hardware software reliability Enhancement of system reliability has been a major concern of computer users and designers and this major revision of the 1982 classic meets users continuing need for practical information on this pressing topic Included are case studies of reliable systems from manufacturers such as Tandem Stratus IBM and Digital as well as coverage of special systems such as the Galileo Orbiter fault protection system and AT T telephone switching processors Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office,2001 *Byte* ,1980-04 Conference Record ,1983

Progress in Cryptology - INDOCRYPT 2013 Goutam Paul,Paul Vaudenay,2013-12-02 This book constitutes the refereed proceedings of the 14th International Conference on Cryptology in India INDOCRYPT 2013 held in Mumbai India in December 2013 The 15 revised full papers presented together with 6 short papers the abstracts of 3 invited talks were carefully reviewed and selected from 76 submissions The papers are organized in topical sections on provable security hash functions and signatures side channel attacks symmetric key cryptanalysis key exchange and secret sharing efficient implementation and hardware and coding theory in cryptography *Focus Japan* ,1991 Theory and Design of Digital Computer Systems T.R. Lewin,David L.G. Noakes,2012-12-06 Knowledge A little light expels much darkness \_ Bahya ibn Paquda Duties of the Heart During the early 1970s digital computer techniques concentrated on the computational and interfacing aspects of digital systems and the decade began as the age of both the mainframe computer and the minicomputer Engineers and system designers needed to know the fundamentals of computer operation and how the practical limitations of the architectures of the day the memory size cost and performance could be overcome it was for this reason that this book was first written By 1980 the microprocessor revolution had arrived As a result the microprocessor became a component of a system rather than a system itself and the need to understand the behaviour of the device became of even greater importance to the system designer New developments in mainframe computers were few with networks of minicomputers taking over their role in many instances The 1980 revision of this book took into account the major advances in semiconductor technology that had occurred since it was first published in 1972 and included material relevant to the microprocessor **Edge Computing Acceleration** Patrick Hung,Hongwei Kan,Greg Knopf,2024-12-12 Discover the latest

advances in computer architecture and software at the dawn of the 5G 6G era In Edge Computing Acceleration from 5G to 6G and Beyond distinguished researchers Dr Patrick Hung Hongwei Kan and Greg Knopf deliver a comprehensive overview of personal computer architecture and software design usage in the upcoming 5G decade The authors begin by introducing key components and exploring different hardware acceleration architectures They move on to discuss 5G data security and data integrity and offer a survey of network virtualization technologies including accelerated virtualization technologies The book analyzes 5G 6G system performance investigating key design considerations and trade offs and introducing high level synthesis flow It concludes with chapters exploring design verification and validation flow illustrations of 5G applications based on artificial intelligence and other emerging technologies and offering highlights of emerging 6G research and roadmaps Readers will enjoy the combination of accessible descriptions of new technologies presented side by side as a step by step guide to designing effective 5G systems The book also includes A thorough introduction to key 5G 6G components including new wireless communication protocols edge and fog computing acceleration technologies IoE architectures software designed networks network function virtualization and data security Explorations of various hardware acceleration architectures like FPGA and GPU acceleration architectures Practical discussions of 5G 6G data security data integrity and a survey of network virtualization technologies In depth treatments of 5G 6G system performance key design considerations high level synthesis flow design verification and validation flow Perfect for undergraduate and graduate students in programs related to communications technology engineering and computer science Edge Computing Acceleration from 5G to 6G and Beyond is a must have resource for engineers programmers system architects technical managers communications business executives telco operators and government regulators who regularly interact with cutting edge communications equipment

**Computerworld** ,1989-05-22 For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld s award winning Web site Computerworld com twice monthly publication focused conference series and custom research form the hub of the world s largest global IT media network

**IEICE Transactions on Communications, Electronics, Information, and Systems** ,1991      **From Ought to Is**  
Deborah Rowland,2025-10-20 A groundbreaking guide to achieving sustainable change in today s turbulent and interconnected global landscape In a world increasingly divided by opposing perspectives systemic inertia and rapid societal shifts From Ought to Is Catalysing Change and Movement in a Polarised World offers a transformative approach to navigating change Deborah Rowland challenges conventional change leadership paradigms by urging readers to confront the reality of what is rather than fixating on what ought to be Through this lens she demonstrates how true sustainable change is achieved not by forcing rigid ideals but by fostering courageous honesty and embracing the messy dynamic truths of the present Rooted in decades of research experience and interdisciplinary insights from neuroscience moral philosophy and spiritual tradition From Ought to Is provides a compelling guide to navigating complex systems Rowland illuminates how

acknowledging the full truth of a situation can catalyse collaborative action and new possibilities Through real life case studies and practical exercises readers are invited to engage deeply with their own change scenarios unlocking new possibilities through inclusion truth and adaptive leadership An experiential roadmap for initiating movement in systems organisations and personal lives From Ought to Is Presents an innovative framework for leading sustainable change which utilises a holistic and interdisciplinary approach Offers an adaptive reality based alternative for complex environments which challenges traditional command and control change models Explores the dynamics of polarisation and its impact on leadership collaboration and systemic transformation Addresses pressing global issues such as political and societal polarisation technological disruption and biodiversity collapse Expands on the principles introduced in Deborah Rowland s bestselling Still Moving to address today s unique challenges Ideal for leaders change practitioners and anyone seeking to shift entrenched patterns From Ought to Is supports learning in leadership development programmes and organisational transformation courses It is an essential text for graduate level studies in business organisational behaviour and leadership and serves as a vital resource for executives consultants and change agents      **Scientific and Technical Aerospace Reports** ,1992      **Asian Sources Electronic Components** ,

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