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Rainer Leupers

C Compilers for ASIPs

Automatic Compiler Generation with LISA

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Jon Whittle, Tony Clark, Thomas Kühne



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C Compilers for ASIPs Manuel Hohenauer, Rainer Leupers, 2009-10-13 This book presents a novel approach for Architecture Description Language ADL based instruction set description that enables the automatic retargeting of the complete software toolkit from a single ADL processor model

Optimized ASIP Synthesis from Architecture Description Language Models Oliver Schliebusch, Heinrich Meyr, Rainer Leupers, 2007-04-27 New software tools and a sophisticated methodology above RTL are required to answer the challenges of designing an optimized application specific processor ASIP This book offers an automated and fully integrated implementation flow and compares it to common implementation practice Case studies emphasise that neither the architectural advantages nor the design space of ASIPs are sacrificed for an automated implementation Realizing a building block which fulfils the requirements on programmability and computational power is now efficiently possible for the first time Optimized ASIP Synthesis from Architecture Description Language Models inspires hardware designers as well as application engineers to design powerful ASIPs that will make their SoC designs unique

Computer Systems: Architectures, Modeling, and Simulation Andy Pimentel, Stamatios Vassiliadis, 2004-07-05 This book constitutes the refereed proceedings of the 4th International Workshop on Systems Architectures Modeling and Simulation SAMOS 2004 held in Samos Greece on July 2004 Besides the SAMOS 2004 proceedings the book also presents 19 revised papers from the predecessor workshop SAMOS 2003 The 55 revised full papers presented were carefully reviewed and selected for inclusion in the book The papers are organized in topical sections on reconfigurable computing architectures and implementation and systems modeling and simulation

Application Analysis Tools for ASIP Design Kingshuk Karuri, Rainer Leupers, 2011-06-15 This book introduces a novel design methodology which can significantly reduce the ASIP development effort through high degrees of design automation The key elements of this new design methodology are a powerful application profiler and an automated instruction set customization tool which considerably lighten the burden of mapping a target application to an ASIP architecture in the initial design stages The book includes several design case studies with real life embedded applications to demonstrate how the methodology and the tools can be used in practice for accelerating the overall ASIP design process

Hardware/Software Co-Design Giovanni DeMicheli, M.G. Sami, 2013-11-11 Concurrent design or co design of hardware and software is extremely important for meeting design goals such as high performance that are the key to commercial competitiveness Hardware Software Co Design covers many aspects of the subject including methods and examples for designing 1 general purpose and embedded computing systems based on instruction set processors 2 telecommunication systems using general purpose digital signal processors as well as application specific instruction set processors 3 embedded control systems and applications to automotive electronics The book also surveys the areas of emulation and prototyping systems with field programmable gate array technologies hardware software synthesis and verification and industrial design trends Most contributions emphasize

the design methodology the requirements and state of the art of computer aided co design tools together with current design examples

Retargetable Compiler Technology for Embedded Systems Rainer Leupers, Peter Marwedel, 2013-03-09 It is well known that embedded systems have to be implemented efficiently This requires that processors optimized for certain application domains are used in embedded systems Such an optimization requires a careful exploration of the design space including a detailed study of cost performance tradeoffs In order to avoid time consuming assembly language programming during design space exploration compilers are needed In order to analyze the effect of various software or hardware configurations on the performance retargetable compilers are needed that can generate code for numerous different potential hardware configurations This book provides a comprehensive and up to date overview of the fast developing area of retargetable compilers for embedded systems It describes a large set important tools as well as applications of retargetable compilers at different levels in the design flow Retargetable Compiler Technology for Embedded Systems is mostly self contained and requires only fundamental knowledge in software and compiler design It is intended to be a key reference for researchers and designers working on software compilers and processor optimization for embedded systems

Embedded Software Alberto Sangiovanni-Vincentelli, Joseph Sifakis, 2002-09-25 This book constitutes the refereed proceedings of the Second International Conference on Embedded Software EMSOFT 2002 held in Grenoble France in October 2002 The book presents 13 invited papers by leading researchers and 17 revised full papers selected during a competitive round of reviewing The book spans the whole range of embedded software including operating systems and middleware programming languages and compilers modeling and validation software engineering and programming methodologies scheduling and execution time analysis formal methods and communication protocols and fault tolerance

Building ASIPs: The Mescal Methodology Matthias Gries, Kurt Keutzer, 2006-07-01 An increasing number of system designers are using ASIP s rather than ASIC s to implement their system solutions Building ASIPs The Mescal Methodology gives a simple but comprehensive methodology for the design of these application specific instruction processors ASIPs The key elements of this methodology are judiciously using benchmarking inclusively identifying the architectural space efficiently describing and evaluating the ASIPs comprehensively exploring the design space successfully deploying the ASIP This book includes demonstrations of applications of the methodologies using the Tipi research framework as well as state of the art commercial toolsets from CoWare and Tensilica

Software and Compilers for Embedded Systems Henk Schepers, 2004-10-29 This volume contains the proceedings of the 8th International Workshop on Software and Compilers for Embedded Systems SCOPES 2004 held in Amsterdam The Netherlands on September 2 and 3 2004 Initially the workshop was referred to as the International Workshop on Code Generation for Embedded Systems The first took place in 1994 in Schloss Dagstuhl Germany From its beginnings the intention of the organizers has been to create an interactive atmosphere in which the participants can discuss and profit from the assembly of international experts in the field The name SCOPES has been used since the fourth edition in Stuttgart many

in 1999 when the scope of the workshop was extended to also cover general issues in embedded software design. Since then SCOPES has been held again in St. Goar in 2001, Berlin, Germany in 2002, Vienna, Austria in 2003 and now in Amsterdam, The Netherlands. In response to the call for papers, almost 50 very strong papers were submitted from all over the world. All submitted papers were reviewed by at least three experts to ensure the quality of the workshop. In the end, the program committee selected 17 papers for presentation at the workshop. These papers are divided into the following categories: application specific co-design, system and application synthesis, data flow analysis, data partitioning, task scheduling, and code generation. In addition to the selected contributions, the keynote address was delivered by Mike Uhler from MIPS Technologies. An abstract of his talk is also included in this volume.

[A Practical Introduction to Hardware/Software Codesign](#) Patrick R. Schaumont, 2012-11-27 This textbook serves as an introduction to the subject of embedded systems design with emphasis on integration of custom hardware components with software. The key problem addressed in the book is the following: how can an embedded systems designer strike a balance between flexibility and efficiency? The book describes how combining hardware design with software design leads to a solution to this important computer engineering problem. The book covers four topics: hardware/software codesign fundamentals, the design space of custom architectures, the hardware/software interface, and application examples. The book comes with an associated design environment that helps the reader to perform experiments in hardware/software codesign. Each chapter also includes exercises and further reading suggestions. Improvements in this second edition include labs and examples using modern FPGA environments from Xilinx and Altera, which will make the material in this book applicable to a greater number of courses where these tools are already in use. More examples and exercises have been added throughout the book. If I were teaching a course on this subject, I would use this as a resource and text. If I were a student who wanted to learn codesign, I would look for a course that at least used a similar approach. If I were an engineer or engineering manager who wanted to learn more about codesign from a very practical perspective, I would read this book first before any other. When I first started learning about codesign as a practitioner, a book like this would have been the perfect introduction.

Grant Martin Tensilica *Language-driven Exploration and Implementation of Partially Re-configurable ASIPs* Anupam Chattopadhyay, Rainer Leupers, Heinrich Meyr, Gerd Ascheid, 2008-12-02 Increasing complexity of modern embedded systems demands system designers to ramp up their design productivity without compromising performance goals. This is promoted by modern Electronic System Level (ESL) techniques. *Language-driven Exploration and Implementation of Partially Re-configurable ASIPs* addresses an important segment of the ESL area by modeling partially re-configurable processors via high-level Architecture Description Language (ADL). This approach also hints at an imminent evolution in the area of re-configurable system design.

[Customizable Embedded Processors](#) Paolo Ienne, Rainer Leupers, 2006-08-30 Customizable processors have been described as the next natural step in the evolution of the microprocessor business: a step in the life of a new technology where top performance

alone is no longer sufficient to guarantee market success Other factors become fundamental such as time to market convenience energy efficiency and ease of customization This book is the first to explore comprehensively one of the most fundamental trends which emerged in the last decade to treat processors not as rigid fixed entities which designers include as is in their products but rather to build sound methodologies to tailor fit processors to the specific needs of such products This book addresses the goal of maintaining a very large family of processors with a wide range of features at a cost comparable to that of maintaining a single processor First book to present comprehensively the major ASIP design methodologies and tools without any particular bias Written by most of the pioneers and top international experts of this young domain Unique mix of management perspective technical detail research outlook and practical implementation

Embedded Computer Systems: Architectures, Modeling, and Simulation Mladen Berekovic, Nikitas

Dimopoulos, Stephan Wong, 2008-07-07 This book constitutes the refereed proceedings of the 8th International Workshop on Systems Architectures Modeling and Simulation SAMOS 2008 held in Samos Greece in July 2008 The 24 revised full papers presented together with a contemplative keynote and additional papers of two special workshop sessions were carefully reviewed and selected from 62 submissions The papers are organized in topical sections on architecture new frontiers SoC application specific contributions system level design for heterogeneous systems programming multicores sensors and sensor networks and systems modeling and design Structured Object-Oriented Formal Language and Method Cong Tian, Fumiko

Nagoya, Shaoying Liu, Zhenhua Duan, 2018-04-20 This book constitutes the thoroughly refereed workshop proceedings of the 7th International Workshop on Structured Object Oriented Formal Language and Method SOFL MSVL 2017 held in Xi an China in November 2017 The 13 revised full papers included in the volume were carefully reviewed and selected from 21 submissions They are organized in the following topical sections animation and prototyping graph theory model checking modeling and specification and verification and validation **Embedded Computing** Joseph A. Fisher, Paolo

Faraboschi, Cliff Young, 2005 Embedded Computing is enthralling in its clarity and exhilarating in its scope If the technology you are working on is associated with VLIWs or embedded computing then clearly it is imperative that you read this book If you are involved in computer system design or programming you must still read this book because it will take you to places where the views are spectacular You don t necessarily have to agree with every point the authors make but you will understand what they are trying to say and they will make you think From the Foreword by Robert Colwell R E Colwell Assoc Inc The fact that there are more embedded computers than general purpose computers and that we are impacted by hundreds of them every day is no longer news What is news is that their increasing performance requirements complexity and capabilities demand a new approach to their design Fisher Faraboschi and Young describe a new age of embedded computing design in which the processor is central making the approach radically distinct from contemporary practices of embedded systems design They demonstrate why it is essential to take a computing centric and system design approach to

the traditional elements of nonprogrammable components peripherals interconnects and buses These elements must be unified in a system design with high performance processor architectures microarchitectures and compilers and with the compilation tools debuggers and simulators needed for application development In this landmark text the authors apply their expertise in highly interdisciplinary hardware software development and VLIW processors to illustrate this change in embedded computing VLIW architectures have long been a popular choice in embedded systems design and while VLIW is a running theme throughout the book embedded computing is the core topic Embedded Computing examines both in a book filled with fact and opinion based on the authors many years of R D experience Features Complemented by a unique professional quality embedded tool chain on the authors website <http://www.vliw.org/book> Combines technical depth with real world experience Comprehensively explains the differences between general purpose computing systems and embedded systems at the hardware software tools and operating system levels Uses concrete examples to explain and motivate the trade offs

Design of Energy-Efficient Application-Specific Instruction Set Processors Tilman Glökler, Heinrich Meyr, 2007-05-08 After a brief introduction to low power VLSI design the design space of ASIP instruction set architectures ISAs is introduced with a special focus on important features for digital signal processing Based on the degrees of freedom offered by this design space a consistent ASIP design flow is proposed this design flow starts with a given application and uses incremental optimization of the ASIP hardware of ASIP coprocessors and of the ASIP software by using a top down approach and by applying application specific modifications on all levels of design hierarchy A broad range of real world signal processing applications serves as vehicle to illustrate each design decision and provides a hands on approach to ASIP design Finally two complete case studies demonstrate the feasibility and the efficiency of the proposed methodology and quantitatively evaluate the benefits of ASIPs in an industrial context

Model Driven Engineering Languages and Systems Jon Whittle, Tony Clark, Thomas Kühne, 2011-10-12 This book constitutes the refereed proceedings of the 14th International Conference on Model Driven Engineering Languages and Systems MODELS 2011 held in Wellington New Zealand in October 2011 The papers address a wide range of topics in research foundations track and practice applications track For the first time a new category of research papers vision papers are included presenting outside the box thinking The foundations track received 167 full paper submissions of which 34 were selected for presentation Out of these 3 papers were vision papers The application track received 27 submissions of which 13 papers were selected for presentation The papers are organized in topical sections on model transformation model complexity aspect oriented modeling analysis and comprehension of models domain specific modeling models for embedded systems model synchronization model based resource management analysis of class diagrams verification and validation refactoring models modeling visions logics and modeling development methods and model integration and collaboration

Reconfigurable Computing: Architectures and Applications Koen Bertels, João M.P. Cardoso, Stamatis Vassiliadis, 2006-08-03 This book constitutes the thoroughly refereed

post proceedings of the Second International Workshop on Reconfigurable Computing ARC 2006 held in Delft The Netherlands in March 2006 The 22 revised full papers and 35 revised short papers presented were thoroughly reviewed and selected from 95 submissions The papers are organized in topical sections on applications power image processing organization and architecture networks and communication security and tools *Software and Compilers for Embedded Systems* Andreas Krall, 2003-10-24 This volume contains the proceedings of the 7th International Workshop on Software and Compilers for Embedded Systems SCOPES 2003 held in Vienna Austria September 24-26 2003 Initially the workshop was referred to as the International Workshop on Code Generation for Embedded Systems The first workshop took place in 1994 in Schloss Dagstuhl Germany From its beginnings the intention of the organizers was to create an atmosphere in which the researchers could participate actively in dynamic discussions and profit from the assembly of international experts in the field It was at the fourth workshop in St Goar Germany in 1999 that the spectrum of topics of interest for the workshop was extended and not only code generation but also software and compilers for embedded systems were considered The change in fields of interest led to a change of name and this is when the present name was used for the first time Since then SCOPES has been held again in St Goar Germany in 2001 Berlin Germany in 2002 and this year 2003 in Vienna Austria In response to the call for papers 43 very strong papers from all over the world were submitted The program committee selected 26 papers for presentation at SCOPES 2003 All submitted papers were reviewed by at least three experts in order to ensure the quality of the work presented at the workshop Synthesis of Arithmetic Circuits Jean-Pierre Deschamps, Gery J.A. Bioul, Gustavo D. Sutter, 2006-03-24 A new approach to the study of arithmetic circuits In *Synthesis of Arithmetic Circuits* FPGA ASIC and Embedded Systems the authors take a novel approach of presenting methods and examples for the synthesis of arithmetic circuits that better reflects the needs of today's computer system designers and engineers Unlike other publications that limit discussion to arithmetic units for general purpose computers this text features a practical focus on embedded systems Following an introductory chapter the publication is divided into two parts The first part Mathematical Aspects and Algorithms includes mathematical background number representation addition and subtraction multiplication division other arithmetic operations and operations in finite fields The second part *Synthesis of Arithmetic Circuits* includes hardware platforms general principles of synthesis adders and subtractors multipliers dividers and other arithmetic primitives In addition the publication distinguishes itself with A separate treatment of algorithms and circuits a more useful presentation for both software and hardware implementations Complete executable and synthesizable VHDL models available on the book's companion Web site allowing readers to generate synthesizable descriptions Proposed FPGA implementation examples namely synthesizable low level VHDL models for the Spartan II and Virtex families Two chapters dedicated to finite field operations This publication is a must have resource for students in computer science and embedded system designers engineers and researchers in the field of hardware and software computer system design and development An Instructor

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