

# **Computer-Aided Reasoning ACL2 Case Studies**

edited by  
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# Computer Aided Reasoning Acl2 Case Studies Advances In Formal Methods

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## **Computer Aided Reasoning Acl2 Case Studies Advances In Formal Methods:**

**Computer-Aided Reasoning** Matt Kaufmann, Panagiotis Manolios, J Strother Moore, 2013-04-17 Computer Aided Reasoning ACL2 Case Studies illustrates how the computer aided reasoning system ACL2 can be used in productive and innovative ways to design build and maintain hardware and software systems Included here are technical papers written by twenty one contributors that report on self contained case studies some of which are sanitized industrial projects The papers deal with a wide variety of ideas including floating point arithmetic microprocessor simulation model checking symbolic trajectory evaluation compilation proof checking real analysis and several others Computer Aided Reasoning ACL2 Case Studies is meant for two audiences those looking for innovative ways to design build and maintain hardware and software systems faster and more reliably and those wishing to learn how to do this The former audience includes project managers and students in survey oriented courses The latter audience includes students and professionals pursuing rigorous approaches to hardware and software engineering or formal methods Computer Aided Reasoning ACL2 Case Studies can be used in graduate and upper division undergraduate courses on Software Engineering Formal Methods Hardware Design Theory of Computation Artificial Intelligence and Automated Reasoning The book is divided into two parts Part I begins with a discussion of the effort involved in using ACL2 It also contains a brief introduction to the ACL2 logic and its mechanization which is intended to give the reader sufficient background to read the case studies A more thorough textbook introduction to ACL2 may be found in the companion book Computer Aided Reasoning An Approach The heart of the book is Part II where the case studies are presented The case studies contain exercises whose solutions are on the Web In addition the complete ACL2 scripts necessary to formalize the models and prove all the properties discussed are on the Web For example when we say that one of the case studies formalizes a floating point multiplier and proves it correct we mean that not only can you read an English description of the model and how it was proved correct but you can obtain the entire formal content of the project and replay the proofs if you wish with your copy of ACL2 ACL2 may be obtained from its home page The results reported in each case study as ACL2 input scripts as well as exercise solutions for both books are available from this page

**Computer-Aided Reasoning** Matt Kaufmann, Panagiotis Manolios, J Strother Moore, 2000-06-30 Computer Aided Reasoning ACL2 Case Studies illustrates how the computer aided reasoning system ACL2 can be used in productive and innovative ways to design build and maintain hardware and software systems Included here are technical papers written by twenty one contributors that report on self contained case studies some of which are sanitized industrial projects The papers deal with a wide variety of ideas including floating point arithmetic microprocessor simulation model checking symbolic trajectory evaluation compilation proof checking real analysis and several others Computer Aided Reasoning ACL2 Case Studies is meant for two audiences those looking for innovative ways to design build and maintain hardware and software systems faster and more reliably and those wishing to learn how to do this The former audience includes project managers

and students in survey oriented courses The latter audience includes students and professionals pursuing rigorous approaches to hardware and software engineering or formal methods Computer Aided Reasoning ACL2 Case Studies can be used in graduate and upper division undergraduate courses on Software Engineering Formal Methods Hardware Design Theory of Computation Artificial Intelligence and Automated Reasoning The book is divided into two parts Part I begins with a discussion of the effort involved in using ACL2 It also contains a brief introduction to the ACL2 logic and its mechanization which is intended to give the reader sufficient background to read the case studies A more thorough textbook introduction to ACL2 may be found in the companion book Computer Aided Reasoning An Approach The heart of the book is Part II where the case studies are presented The case studies contain exercises whose solutions are on the Web In addition the complete ACL2 scripts necessary to formalize the models and prove all the properties discussed are on the Web For example when we say that one of the case studies formalizes a floating point multiplier and proves it correct we mean that not only can you read an English description of the model and how it was proved correct but you can obtain the entire formal content of the project and replay the proofs if you wish with your copy of ACL2 ACL2 may be obtained from its home page The results reported in each case study as ACL2 input scripts as well as exercise solutions for both books are available from this page

**Computer-Aided Reasoning** Matt Kaufmann, Panagiotis Manolios, J Strother Moore, 2012-12-06 Computer Aided Reasoning An Approach is a textbook introduction to computer aided reasoning It can be used in graduate and upper division undergraduate courses on software engineering or formal methods It is also suitable in conjunction with other books in courses on hardware design discrete mathematics or theory especially courses stressing formalism rigor or mechanized support It is also appropriate for courses on artificial intelligence or automated reasoning and as a reference for business and industry Current hardware and software systems are often very complex and the trend is towards increased complexity Many of these systems are of critical importance therefore making sure that they behave as expected is also of critical importance By modeling computing systems mathematically we obtain models that we can prove behave correctly The complexity of computing systems makes such proofs very long complicated and error prone To further increase confidence in our reasoning we can use a computer program to check our proofs and even to automate some of their construction In this book we present A practical functional programming language closely related to Common Lisp which is used to define functions which can model computing systems and to make assertions about defined functions A formal logic in which defined functions correspond to axioms the logic is first order includes induction and allows us to prove theorems about the functions The computer aided reasoning system ACL2 which includes the programming language the logic and mechanical support for the proof process The ACL2 system has been successfully applied to projects of commercial interest including microprocessor modeling hardware verification microcode verification and software verification This book gives a methodology for modeling computing systems formally and for reasoning about those models with mechanized assistance The practicality of computer

aided reasoning is further demonstrated in the companion book *Computer Aided Reasoning ACL2 Case Studies*. Approximately 140 exercises are distributed throughout the book. Additional material is freely available from the ACL2 home page on the Web including solutions to the exercises, additional exercises, case studies from the companion book, research papers, and the ACL2 system with detailed documentation.

*Formal Methods in Computer-Aided Design* Warren A. Jr. Hunt, Steven D. Johnson, 2007-11-29. The biannual Formal Methods in Computer Aided Design conference FMCAD 2000 is the third in a series of conferences under that title devoted to the use of discrete mathematical methods for the analysis of computer hardware and software. The work reported in this book describes the use of modeling languages and their associated automated analysis tools to specify and verify computing systems. Functional verification has become one of the principal costs in a modern computer design effort. In addition, verification of circuit models, timing, power, etc. requires even more effort. FMCAD provides a venue for academic and industrial researchers and practitioners to share their ideas and experiences of using discrete mathematical modeling and verification. It is noted with interest by the conference chairmen how this area has grown from just a few people 15 years ago to a vibrant area of research, development, and deployment. It is clear that these methods are helping reduce the cost of designing computing systems. As an example of this potential cost reduction, we have invited David Russino of Advanced Micro Devices Inc. to describe his verification of floating point algorithms being used in AMD microprocessors. The program includes 30 regular presentations selected from 63 submitted papers.

**Computer Aided Verification** Rajeev Alur, 2004-06-29. This book constitutes the refereed proceedings of the 16th International Conference on Computer Aided Verification CAV 2004 held in Boston, MA, USA, in July 2004. The 32 revised full research papers and 16 tool papers were carefully reviewed and selected from 144 submissions. The papers cover all current issues in computer aided verification and model checking, ranging from foundational and methodological issues to the evaluation of major tools and systems.

Automated Reasoning Ulrich Furbach, Natarajan Shankar, 2006-10-06. Here are the proceedings of the Third International Joint Conference on Automated Reasoning IJCAR 2006 held in Seattle, Washington, USA, August 2006. The book presents 41 revised full research papers and 8 revised system descriptions with 3 invited papers and a summary of a systems competition. The papers are organized in topical sections on proofs, search, higher order logic, proof theory, proof checking, combination, decision procedures, CASC, J3, rewriting, and description logic.

Automated Deduction -- CADE-24 Maria Paola Bonacina, 2013-06-04. This book constitutes the proceedings of the 24th International Conference on Automated Deduction CADE 24 held in Lake Placid, NY, USA, in June 2013. The 31 revised full papers presented together with 2 invited papers were carefully reviewed and selected from 71 initial submissions. CADE is the major forum for the presentation of research in all aspects of automated deduction, ranging from theoretical and methodological issues to the presentation of new theorem provers, solvers, and systems.

**Formal Methods for Hardware Verification** Marco Bernardo, Alessandro Cimatti, 2006-05-15. This book presents 8 papers accompanying the lectures of leading researchers

given at the 6th edition of the International School on Formal Methods for the Design of Computer Communication and Software Systems SFM 2006 SFM 2006 was devoted to formal techniques for hardware verification and covers several aspects of the hardware design process including hardware design languages and simulation property specification formalisms automatic test pattern generation symbolic trajectory evaluation and more *Formal Methods and Software Engineering* Jin Song Dong, Jim Woodcock, 2003-11-06 This volume contains the proceedings of the 2003 International Conference on Formal Engineering Methods ICFEM 2003 The conference was the fifth in a series that began in 1997 ICFEM 2003 was held in Singapore during 5-7 November 2003 ICFEM 2003 aimed to bring together researchers and practitioners from industry academia and government to advance the state of the art in formal engineering methods and to encourage a wider uptake of formal methods in industry The Program Committee received 91 submissions from more than 20 countries in various regions After each paper was reviewed by at least three referees in each relevant field 34 high quality papers were accepted based on originality technical content presentation and relevance to formal methods and software engineering We wish to sincerely thank all authors who submitted their work for consideration We would also like to thank the Program Committee members and other reviewers for their great efforts in the reviewing and selecting process We are indebted to the three keynote speakers Prof Ian Hayes of the University of Queensland Prof Mathai Joseph of the Tata Research Development and Design Centre and Dr Colin O'Halloran of QinetiQ for accepting our invitation to address the conference

**NASA Formal Methods** Alwyn Goodloe, Suzette Person, 2012-03-27 This book constitutes the refereed proceedings of the Fourth International Symposium on NASA Formal Methods NFM 2012 held in Norfolk VA USA in April 2012 The 36 revised regular papers presented together with 10 short papers 3 invited talks were carefully reviewed and selected from 93 submissions The topics are organized in topical sections on theorem proving symbolic execution model based engineering real time and stochastic systems model checking abstraction and abstraction refinement compositional verification techniques static and dynamic analysis techniques fault protection cyber security specification formalisms requirements analysis and applications of formal techniques *Integrated Formal Methods* Eerke Boiten, John Derrick, Graeme Smith, 2004-03-05 The fourth conference in the series of international meetings on Integrated Formal Methods IFM was held in Canterbury UK 4-7 April 2004 The conference was organized by the Computing Laboratory at the University of Kent whose main campus is just outside the ancient town of Canterbury part of the county of Kent Kent is situated in the southeast of England and the university sits on a hill overlooking the city of Canterbury and its world renowned cathedral The University of Kent was granted its Royal Charter in 1965 Today there are almost 10 000 full time and part time students with over 110 nationalities represented The IFM meetings have proven to be particularly successful The first meeting was held in York in 1999 and subsequently we held events in Germany in 2000 and then Finland in 2002 The conferences are held every 18 months or so and attract a wide range of participants from Europe the Americas Asia and Australia The conference is now

rmly part of the formal methods conference calendar The conference has also evolved in terms of themes and subjects presented and this year in line with the subject as a whole we saw more work on veri cation as some of the challenges in this subject are being met The work reported at IFM conferences can be seen as part of the attempt to manage complexity by combining paradigms of speci cation and design so that the most appropriate design tools are used at di erent points in the life cycle Proceedings of the Sixth International Workshop on the ACL2 Theorem Prover and its Applications ,2006

*Correct Hardware Design and Verification Methods* Dominique Borrione,Wolfgang Paul,2005-10-07 This book constitutes the refereed proceedings of the 13th IFIP WG 10.5 Advanced Research Working Conference on Correct Hardware Design and Verification Methods CHARME 2005 held in Saarbrücken Germany in October 2005 The 21 revised full papers and 18 short papers presented together with 2 invited talks and one tutorial were carefully reviewed and selected from 79 submissions The papers are organized in topical sections on functional approaches to design description game solving approaches abstraction algorithms and techniques for speeding DD based verification real time and LTL model checking evaluation of SAT based tools model reduction and verification of memory hierarchy mechanisms *Correct Hardware Design and Verification Methods* Daniel Geist,Enrico Tronci,2003-10-10 This book constitutes the refereed proceedings of the 12th IFIP WG 10.5 Advanced Research Working Conference on Correct Hardware Design and Verification Methods CHARME 2003 held in L'Aquila Italy in October 2003 The 24 revised full papers and 8 short papers presented were carefully reviewed and selected from 65 submissions The papers are organized in topical sections on software verification automata based methods processor verification specification methods theorem proving bounded model checking and model checking and applications **Computer-aided Verification** ,2004 **Theorem Proving in Higher Order Logics** David

Basin,Burkhart Wolff,2003-08-21 This volume constitutes the proceedings of the16th International Conference on Theorem Proving in Higher Order Logics TPHOLs 2003 held September 8-12 2003 in Rome Italy TPHOLs covers all aspects of theorem proving in higher order logics as well as related topics in theorem proving and veri cation TPHOLs 2003 was co located with TABLEAUX the International Conference on Automated Reasoning with Analytic Tableaux and Related Methods and with Calculemus the Symposium on the Integration of Symbolic Computation and Mechanized Reasoning There were 50 papers submitted to TPHOLs in the full research category each of which was refereed by at least 3 reviewers selected by the program committee Ofthesesubmissions 21wereacceptedforpresentationattheconference and publication in this volume In keeping with tradition TPHOLs 2003 also offered a venue for the presentation of work in progress where researchers vite discussion by means of a brief preliminary talk and then discuss their work at a poster session A supplementary proceedings containing associated papers for work in progress was published by the computer science department at the Universit at Freiburg The organizers are grateful to Jean Raymond Abrial Patrick Lincoln and Dale Miller for agreeing to give invited talks at TPHOLs 2003 The TPHOLs conference traditionally changes continent each year in order to maximize the chances

that researchers from around the world can attend Practical Aspects of Declarative Languages Andy Gill, Terrance Swift, 2009-01-10 Declarative languages have long promised the ability to rapidly create easily maintainable software for complex applications The International Symposium of Practical Aspects of Declarative Languages PADL provides a yearly forum for presenting results on the principles the implementations and especially the applications of declarative languages The PADL symposium held January 19 20 2009 in Savannah Georgia was the 11th in this series This year 48 papers were submitted from authors in 17 countries The Program Committee performed outstandingly to ensure that each of these papers submitted to PADL 2009 was thoroughly reviewed by at least three referees in a short period of time The resulting symposium presented a microcosm of how the current generation of declarative languages are being used to address real applications along with on going work on the languages themselves The program also included two invited talks *Inspecting and Preferring Abductive Models* by Luis Moniz Pereira and *Applying Declarative Languages to Commercial Hardware Design* by Je Lewis Regular papers presented a variety of applications including distributed applications over networks network verification user interfaces visualization in astrophysics nucleotide sequence analysis and planning under incomplete information PADL 2009 also included ongoing work on the declarative languages themselves Multi threaded and concurrent Prolog implementation was addressed in several papers as were innovations for tabling in Prolog and functional arrays in Haskell Recent applications have also sparked papers on meta predicates in Prolog and a module system for ACL2

**Design and Verification of Microprocessor Systems for High-Assurance Applications** David S. Hardin, 2010-03-02 Microprocessors increasingly control and monitor our most critical systems including automobiles airliners medical systems transportation grids and defense systems The relentless march of semiconductor process technology has given engineers exponentially increasing transistor budgets at constant recurring cost This has encouraged increased functional integration onto a single die as well as increased architectural sophistication of the functional units themselves Additionally design cycle times are decreasing thus putting increased schedule pressure on engineers Not surprisingly this environment has led to a number of uncaught design flaws Traditional simulation based design verification has not kept up with the scale or pace of modern microprocessor system design Formal verification methods offer the promise of improved bug finding capability as well as the ability to establish functional correctness of a detailed design relative to a high level specification However widespread use of formal methods has had to await breakthroughs in automated reasoning integration with engineering design languages and processes scalability and usability This book presents several breakthrough design and verification techniques that allow these powerful formal methods to be employed in the real world of high assurance microprocessor system design

*Practical Aspects of Declarative Languages* Shriram Krishnamurthi, C.R. Ramakrishnan, 2003-06-30 Declarative languages build on sound theoretical bases to provide attractive frameworks for application development These languages have been successfully applied to a wide variety of real world situations including database management active networks software



engineering and decision support systems New developments in theory and implementation expose fresh opportunities At the same time the application of declarative languages to novel problems raises numerous interesting research issues These well known questions include scalability language extensions for application deployment and programming environments Thus applications drive the progress in the theory and implementation of declarative systems and in turn benefit from this progress The International Symposium on Practical Applications of Declarative Languages PADL provides a forum for researchers practitioners and implementors of declarative languages to exchange ideas on current and novel applications and on the requirements for effective use of declarative systems The fourth PADL symposium was held in Portland Oregon on January 19 and 20 2002 *Proceedings*, 2004

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