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					Rows 1 to 45
Object Name	Object Type	Product	Schema Owner	Description	
ICX_POR_ITEMS	TABLE	ICX - Oracle Procurement	ICX	Stores information and cost descriptors for non brand data for each item in the catalog. Most descriptor cols in the format XXXX where XXXX is an integer. Some descriptors are seeded by Oracle while others may be	
ICX_POR_LOADER_VALUES	TABLE	ICX - Oracle Procurement	ICX	Stores a single record with the setup data for the Eut process.	
ICX_POR_CONTRACT_REFERENCES	TABLE	ICX - Oracle Procurement	ICX	Stores contract information for loader job.	
AP_POL_EXRATE_OPTIONS_ALL	TABLE	AP - Payables	AP	This table stores exchange rate tolerances for policy compliance validation.	
POBIV_EDW_CONTRACT_AGRMNTS_FCV	VIEW	PO - Purchasing		EDW Contract Agreement Fact Collection View	
PO_DISTRIBUTIONS_ALL	TABLE	PO - Purchasing	PO	Purchase order distributions	
PO_NOTIFICATION_CTRL_DRAFT	TABLE	PO - Purchasing	PO	Blanket agreement and planned purchase order notification control draft table	
AMM_COMPLIANCE_ENVS_B	TABLE	AMM - Internal Controls Manager (Obsolete)		Stores compliance details	
AS_OSI_LEADS_DENORM	TABLE	AS - Sales Foundation	OSM	Denormalized table	
WMS_PARAMETERS_B	TABLE	WMS - Warehouse Management	WMS	WMS Parameters	
POH_AUC_ITEMS_INTERFACE	TABLE	POH - Sourcing	POH	Interface table to store imported negotiation item info	
POA_EDW_PO_DIST_F	TABLE	POA - Purchasing Intelligence (Obsolete)		PO Distributions fact table	
POA_EDW_PO_DIST_FSTG	TABLE	POA - Purchasing Intelligence (Obsolete)		Interface table for PO Distributions fact	
OKC_K_GRPINGS_V	VIEW	OKC - Contracts Core	APPS	View for table OKC_CONTRACT_GROUPINGS	
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Electronic Technical Reference Manual

**Luciano Lavagno, Igor L. Markov, Grant
Martin, Louis K. Scheffer**



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Embedded and Ubiquitous Computing Edwin Sha,Sung-Kook Han,Cheng-Zhong Xu,Moon Hae Kim,Laurence T. Yang,Bin Xiao,2006-10-06 This book constitutes the refereed proceedings of the International Conference on Embedded and Ubiquitous Computing EUC 2006 held in Seoul Korea August 2006 The book presents 113 revised full papers together with 3 keynote articles organized in topical sections on power aware computing security and fault tolerance agent and distributed computing wireless communications real time systems embedded systems multimedia and data management mobile computing network protocols middleware and P2P and more *The AT&T Documentation Guide* ,1993-06 Catalog of the most often requested AT T documents *Visual Perception for Humanoid Robots* David Israel González Aguirre,2018-09-01 This book provides an overview of model based environmental visual perception for humanoid robots The visual perception of a humanoid robot creates a bidirectional bridge connecting sensor signals with internal representations of environmental objects The objective of such perception systems is to answer two fundamental questions What where is it To answer these questions using a sensor to representation bridge coordinated processes are conducted to extract and exploit cues matching robot s mental representations to physical entities These include sensor actuator modeling calibration filtering and feature extraction for state estimation This book discusses the following topics in depth Active Sensing Robust probabilistic methods for optimal high dynamic range image acquisition are suitable for use with inexpensive cameras This enables ideal sensing in arbitrary environmental conditions encountered in human centric spaces The book quantitatively shows the importance of equipping robots with dependable visual sensing Feature Extraction Recognition Parameter free edge extraction methods based on structural graphs enable the representation of geometric primitives effectively and efficiently This is done by eccentricity segmentation providing excellent recognition even on noisy low resolution images Stereoscopic vision Euclidean metric and graph shape descriptors are shown to be powerful mechanisms for difficult recognition tasks Global Self Localization Depth Uncertainty Learning Simultaneous feature matching for global localization and 6D self pose estimation are addressed by a novel geometric and probabilistic concept using intersection of Gaussian spheres The path from intuition to the closed form optimal solution determining the robot location is described including a supervised learning method for uncertainty depth modeling based on extensive ground truth training data from a motion capture system The methods and experiments are presented in self contained chapters with comparisons and the state of the art The algorithms were implemented and empirically evaluated on two humanoid robots ARMAR III A B The excellent robustness performance and derived results received an award at the IEEE conference on humanoid robots and the contributions have been utilized for numerous visual manipulation tasks with demonstration at distinguished venues such as ICRA CeBIT IAS and Automatica

Electronic Design Automation for IC System Design, Verification, and Testing Luciano Lavagno, Igor L. Markov, Grant Martin, Louis K. Scheffer, 2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

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