Functional Decomposition with Application to FPGA Synthesis

Christoph Scholl

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Jason Cong,Yean-Yow Hwang

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Functional Decomposition with Applications to FPGA Synthesis Christoph Scholl,2013-03-09 During the last few years Field Programmable Gate Arrays FPGAs have become increasingly important Thanks to recent breakthroughs in technology FPGAs offer millions of system gates at low cost and considerable speed Functional decomposition has emerged as an essential technique in automatic logic synthesis for FPGAs Functional decomposition as a technique to find realizations for Boolean functions was already introduced in the late fifties and early sixties by Ashenhurst Curtis Roth and Karp In recent years however it has attracted a great deal of renewed attention for several reasons First it is especially well suited for the synthesis of lookup table based FPGAs Also the increased capacities of today s computers as well as the development of new methods have made the method applicable to larger scale problems Modern techniques for functional decomposition profit from the success of Reduced Ordered Binary Decision Diagrams ROBDDs data structures that provide compact representations for many Boolean functions occurring in practical applications We have now seen the development of algorithms for functional decomposition which work directly based on ROBDDs so that the decomposition algorithm works based on compact representations and not on function tables or decomposition matrices as in previous approaches The book presents in a consistent manner a comprehensive presentation of a multitude of results stemming from the author s as well as various researchers work in the field Apart from the basic method it also covers functional decomposition for incompletely specified functions decomposition for multi output functions and non disjoint decomposition Functional Decomposition with Application to FPGA Synthesis will be of interest both to researchers and advanced students in logic synthesis VLSI CAD and Design Automation as well as professionals working in FPGA design and the developmentof algorithms for FPGA synthesis

Communication Based FPGA Synthesis for Multi-output Boolean Function Christoph Scholl,Paul Molitor,1995 **A Theory on Partially-dependent Function Decomposition with Application in LUT-based FPGA** Jason Cong,Yean-Yow Hwang,1995 Logic Synthesis for FPGA-Based Mealy Finite State Machines Alexander Barkalov,Larysa Titarenko,Kazimierz Krzywicki,2024-12-04 This book is devoted to the logic synthesis of field programmable gate array FPGA based circuits of Mealy finite state machines FSM Three new methods of state assignment are proposed which allows obtaining FSM circuits required minimum amount of internal chip resources Logic Synthesis for FPGA Based Mealy Finite State Machines Structural Decomposition in Logic Design contains several original synthesis and optimization methods based on the structural decomposition of FPGA based FSM circuits developed by the authors To optimize FSM circuits the authors introduce the use of three methods of state assignment twofold extended and composite These methods allow for the creation of two or three level architectures of FSM circuits The authors also demonstrate how the proposed methods FSM architectures and synthesis methods can replace known solutions based on either functional decomposition or classical methods of structural decomposition The authors also show how these architectures have regular systems of

interconnections and demonstrate positive features compared to methods based on functional decomposition including producing circuits with fewer elements that are faster and consume less power than their counterparts The book includes experimental results proving the efficiency of the proposed solutions and compares the numbers in Look up Tables LUTs showing the performance maximum operating frequency and power consumption for various methods of state assignment The audience for this book is students researchers and engineers specializing in computer science engineering electronics and telecommunications It will be especially useful for engineers working within the scope of algorithms hardware based software accelerators and control units and systems based on the use of FPGAs **Function Decomposition and Synthesis Using Linear Sifting** Christoph Meinel,Fabio Somenzi,Thorsten Theobald,1997 **A Study of Functional Decomposition for Multilevel Logic Synthesis** Kuo-Hua Wang,1994 Logic Synthesis for FPGA-Based Control Units Alexander Barkalov,Larysa Titarenko,Kamil Mielcarek,Sławomir Chmielewski,2020-01-08 This book focuses on control units which are a vital part of modern digital systems and responsible for the efficiency of controlled systems The model of a finite state machine FSM is often used to represent the behavior of a control unit As a rule control units have irregular structures that make it impossible to design their logic circuits using the standard library cells Design methods depend strongly on such factors as the FSM used specific features of the logic elements implemented in the FSM logic circuit and the characteristics of the control algorithm to be interpreted This book discusses Moore and Mealy FSMs implemented with FPGA chips including look up table elements LUT and embedded memory blocks EMB It is crucial to minimize the number of LUTs and EMBs in an FSM logic circuit as well as to make the interconnections between the logic elements more regular and various methods of structural decompositions can be used to solve this problem These methods are reduced to the presentation of an FSM circuit as a composition of different logic blocks the majority of which implement systems of intermediate logic functions different and much simpler than input memory functions and FSM output functions The structural decomposition results in multilevel FSM circuits having fewer logic elements than equivalent single level circuits The book describes well known methods of structural decomposition and proposes new ones examining their impact on the final amount of hardware in an FSM circuit It is of interest to students and postgraduates in the area of Computer Science as well as experts involved in designing digital systems with complex control units The proposed models and design methods open new possibilities for creating logic circuits of control units with an optimal amount of hardware and regular interconnections **Advances in Functional Decomposition** Andrés Martinelli,Kungliga Tekniska högskolan. Institutionen för elektronik, dator- och programvarusystem,2006 Functional Decomposition Edited by: Kisak,2015-10-05 Functional decomposition refers broadly to the process of resolving a functional relationship into its constituent parts in such a way that the original function can be reconstructed i e recomposed from those parts by function composition In general this process of decomposition is undertaken either for the purpose of gaining insight into the identity of the constituent components which may reflect

individual physical processes of interest for example or for the purpose of obtaining a compressed representation of the global function a task which is feasible only when the constituent processes possess a certain level of modularity i e independence or non interaction Interactions between the components are critical to the function of the collection All interactions may not be observable but possibly deduced through repetitive perception synthesis validation and verification of composite behavior **Synthesis and Optimization of FPGA-Based Systems** Valery Sklyarov,Iouliia Skliarova,Alexander Barkalov,Larysa Titarenko,2014-03-19 The book is composed of two parts The first part introduces the concepts of the design of digital systems using contemporary field programmable gate arrays FPGAs Various design techniques are discussed and illustrated by examples The operation and effectiveness of these techniques is demonstrated through experiments that use relatively cheap prototyping boards that are widely available The book begins with easily understandable introductory sections continues with commonly used digital circuits and then gradually extends to more advanced topics The advanced topics include novel techniques where parallelism is applied extensively These techniques involve not only core reconfigurable logical elements but also use embedded blocks such as memories and digital signal processing slices and interactions with general purpose and application specific computing systems Fully synthesizable specifications are provided in a hardware description language VHDL and are ready to be tested and incorporated in engineering designs A number of practical applications are discussed from areas such as data processing and vector based computations e g Hamming weight counters comparators The second part of the book covers the more theoretical aspects of finite state machine synthesis with the main objective of reducing basic FPGA resources minimizing delays and achieving greater optimization of circuits and systems **Effective and Efficient Circuit Synthesis for LUT FPGAs Based on Functional Decomposition and Information Relationship Measures** Artur Chojnacki,2004

Unveiling the Power of Verbal Beauty: An Psychological Sojourn through **Functional Decomposition With Applications To Fpga Synthesis By Scholl Christoph 2010 Paperback**

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