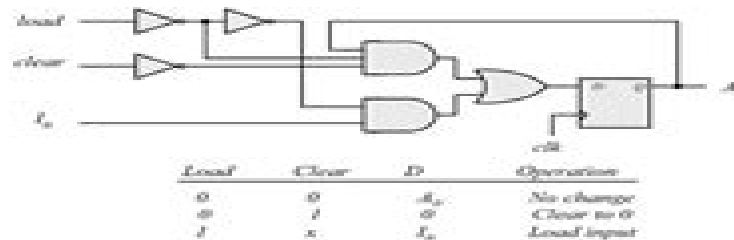


CHAPTER 6

- 6.1** The structure shown below gates the clock through a nand gate. In practice, the circuit can exhibit two problems if the load signal is asynchronous: (1) the gated clock arrives in the setup interval of the clock of the flip-flop, causing metastability, and (2) the load signal truncates the width of the clock pulse. Additionally, the propagation delay through the nand gate might compromise the synchronicity of the overall circuit.



- 6.2** Modify Fig. 6.2, with each stage replicating the first stage shown below:



Note: In this design, *load* has priority over *clear*.

- 6.3** Serial data is transferred one bit at a time. Parallel data is transferred n bits at a time ($n > 1$).
- A shift register can convert serial data into parallel data by first shifting one bit a time into the register and then taking the parallel data from the register outputs.
- A shift register with parallel load can convert parallel data to a serial format by first loading the data in parallel and then shifting the bits one at a time.
- 6.4** $101101 \Rightarrow 1101; 0110; 1011; 1101; 0110; 1011$
- 6.5** (a) See Fig. 11.19: IC 74194
- (b) See Fig. 11.20. Connect two 74194 ICs to form an 8-bit register.

Digital Logic Computer Design Solution Manual

Jin-Ying Zhang



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Solutions manual Gideon Langholz, Abraham Kandel, Joe L. Mott, 1988 **Digital Logic Design B.** Holdsworth, 2014-05-12 Digital Logic Design Second Edition provides a basic understanding of digital logic design with emphasis on the two alternative methods of design available to the digital engineer This book describes the digital design techniques which have become increasingly important Organized into 14 chapters this edition begins with an overview of the essential laws of Boolean algebra K map plotting techniques as well as the simplification of Boolean functions This text then presents the properties and develops the characteristic equations of a number of various types of flip flop Other chapters consider the design of synchronous and asynchronous counters using either discrete flip flops or shift registers This book discusses as well the design and implementation of event driven logic circuits using the NAND sequential equation The final chapter deals with simple coding techniques and the principles of error detection and correction This book is a valuable resource for undergraduate students digital engineers and scientists **Solutions Manual for Digital Logic and State Machine Design** David J. Comer, 1984 **Solutions Manual to Digital Logic and State Machine Design** David J. Comer, 1995 This book is intended for undergraduate students in Electrical Engineering *Introduction to Digital Logic Design* John Patrick Hayes, 1993-01-01 **Introduction to Logic Circuits & Logic Design with Verilog** Brock J. LaMeres, 2017-04-17 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning Goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome **Fundamentals of Computer Organization and Design** Sivarama P. Dandamudi, 2006-05-31 Computer science and engineering curricula have been evolving at a fast pace to keep up with the developments in the area There are separate books available on assembly language programming and computer organization There is a definite need to support the courses that combine assembly language programming and computer organization The book is suitable for a first course in computer organization The style is similar to that of the author s assembly language book in that it strongly supports self study by students This organization facilitates compressed presentation of material Emphasis is also placed on related concepts to practical designs chips Topics and features material presentation suitable for self study concepts related to practical designs and implementations extensive

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