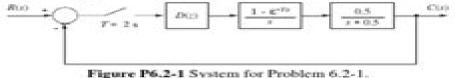
## Digital Control System Analysis and Design 4th Edition Phillips solution Manual

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#### CHAPTER 6

6.2-1. Consider the closed-loop system of Fig. P6.2-1.



- (a) Calculate and plot the unit-step response at the sampling instants, for the case that D(z) = 1.
- (b) Calculate the system unit-step response of the analog system, that is, with the sampler, digital controller, and data hold removed. Plot the response on the same graph with the results of part (a).
- (c) For the system of Fig. P6.2-1, let D(z) = 1 and T = 0.4 s. Calculate the unit-step response andplot these results on the same graph used for parts (a) and (b).
- (d) Use the system dc gains to calculate the steady-state responses for each of the systems of parts (a), (b), and (c). Why are these gains equal?

#### Solution:

# **Digital Control Systems Analysis Solution Manual**

**Richard Bailey** 

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