

DIGITAL ELECTRONICS

Electro Engg. Study Notes

DE BASICS, COMBINATIONAL AND
SEQUENTIAL CIRCUITS, LOGIC FAMILIES

Combinational Circuits

Diagram illustrating Combinational Circuits:

- Inputs: A_1, A_2, A_3
- Outputs: Y_1, Y_2, Y_3
- Logic Function: $Y_1 = A_1 \oplus A_2 \oplus A_3$
- Truth Table:

A_1	A_2	A_3	Y_1
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Sequential Circuits

Diagram illustrating Sequential Circuits:

 - Inputs: D_1, D_2, D_3
 - Outputs: Q_1, Q_2, Q_3
 - Logic Function: $Q_1' = D_1 \oplus Q_2 \oplus Q_3$
 $Q_2' = D_2 \oplus Q_1 \oplus Q_3$
 $Q_3' = D_3 \oplus Q_1 \oplus Q_2$
 - Truth Table:

D_1	D_2	D_3	Q_1'	Q_2'	Q_3'
0	0	0	0	0	0
0	0	1	1	0	0
0	1	0	1	1	0
0	1	1	0	1	1
1	0	0	1	0	1
1	0	1	0	1	1
1	1	0	0	0	1
1	1	1	1	1	0

Logic Families

Diagram illustrating Logic Families:

 - Inputs: A_1, A_2, A_3
 - Outputs: Y_1, Y_2, Y_3
 - Logic Function: $Y_1 = A_1 \oplus A_2 \oplus A_3$
 - Truth Table:

A_1	A_2	A_3	Y_1
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Diagram illustrating Logic Families:

 - Inputs: A_1, A_2, A_3
 - Outputs: Y_1, Y_2, Y_3
 - Logic Function: $Y_1 = A_1 \oplus A_2 \oplus A_3$
 - Truth Table:

A_1	A_2	A_3	Y_1
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Diagram illustrating Logic Families:

 - Inputs: A_1, A_2, A_3
 - Outputs: Y_1, Y_2, Y_3
 - Logic Function: $Y_1 = A_1 \oplus A_2 \oplus A_3$
 - Truth Table:

A_1	A_2	A_3	Y_1
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

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