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Daniela Niemeyer



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Digital Design and Computer Architecture David Harris, Sarah Harris, 2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material

Digital Design and Computer Architecture, RISC-V Edition Sarah Harris, David Harris, 2021-07-12 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with

practical examples that show how to use SparkFun's RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems

Computer Books and Serials in Print, 1985 **Subject Guide to Books in Print**, 1997 Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno, Igor L. Markov, Grant Martin, Louis K. Scheffer, 2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

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