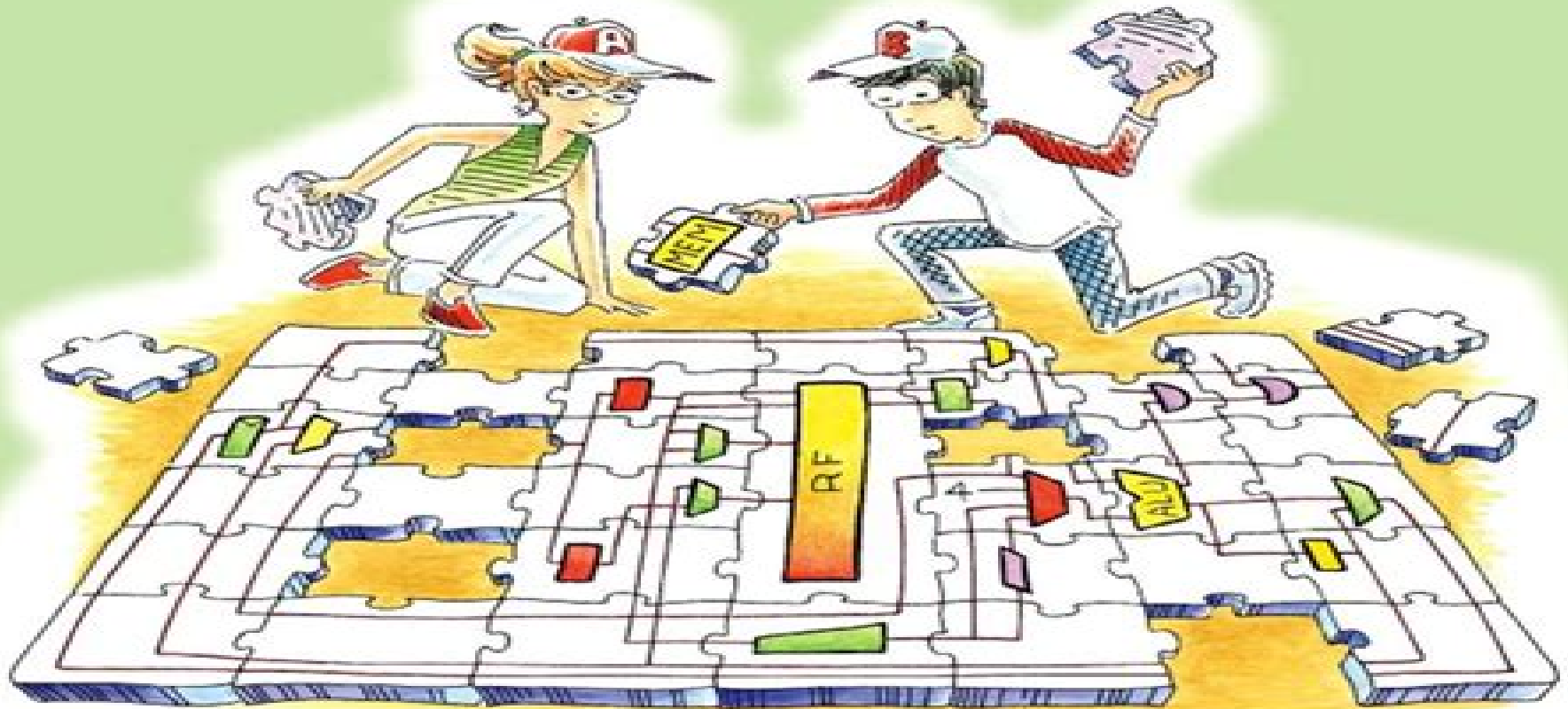


Digital Design and Computer Architecture

SECOND EDITION



David Money Harris & Sarah L. Harris

Digital Design And Computer Architecture Second Edition

David Money Harris, Sarah L. Harris



Digital Design And Computer Architecture Second Edition:

Digital Design and Computer Architecture David Harris, Sarah Harris, 2012-08-24 Digital Design and Computer Architecture Second Edition takes a unique and modern approach to digital design introducing the reader to the fundamentals of digital logic and then showing step by step how to build a MIPS microprocessor in both Verilog and VHDL This new edition combines an engaging and humorous writing style with an updated and hands on approach to digital design It presents new content on I O systems in the context of general purpose processors found in a PC as well as microcontrollers found almost everywhere Beginning with digital logic gates and progressing to the design of combinational and sequential circuits the book uses these fundamental building blocks as the basis for the design of an actual MIPS processor It provides practical examples of how to interface with peripherals using RS232 SPI motor control interrupts wireless and analog to digital conversion SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design There are also additional exercises and new examples of parallel and advanced architectures practical I O applications embedded systems and heterogeneous computing plus a new appendix on C programming to strengthen the connection between programming and processor architecture This new edition will appeal to professional computer engineers and to students taking a course that combines digital logic and computer architecture Updated based on instructor feedback with more exercises and new examples of parallel and advanced architectures practical I O applications embedded systems and heterogeneous computing Presents digital system design examples in both VHDL and SystemVerilog updated for the second edition from Verilog shown side by side to compare and contrast their strengths Includes a new chapter on C programming to provide necessary prerequisites and strengthen the connection between programming and processor architecture Companion Web site includes links to Xilinx CAD tools for FPGA design lecture slides laboratory projects and solutions to exercises Instructors can also register at textbooks.elsevier.com for access to Solutions to all exercises PDF Lab materials with solutions HDL for textbook examples and exercise solutions Lecture slides PPT Sample exams Sample course syllabus Figures from the text JPG PPT

Digital Design and Computer Architecture David Harris, Sarah Harris, 2015-04-22 Digital Design and Computer Architecture ARM Edition takes a unique and modern approach to digital design Beginning with digital logic gates and progressing to the design of combinational and sequential circuits Harris and Harris use these fundamental building blocks as the basis for what follows the design of an actual ARM processor With over 75% of the world s population using products with ARM processors the design of the ARM processor offers an exciting and timely application of digital design while also teaching the fundamentals of computer architecture SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Harris and Harris have combined an engaging and humorous writing

style with an updated and hands on approach to digital design Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader s understanding and retention of key concepts and techniques The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises

Digital Design and Computer Architecture
Sarah Harris,David Harris,2021-08-17 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems

Digital Design and Computer Architecture David Money Harris,Sarah L. Harris,2013 Provides practical examples of how to interface with peripherals using RS232 SPI motor control interrupts wireless and analog to

digital conversion This book covers the fundamentals of digital logic design and reinforces logic concepts through the design of a MIPS microprocessor *Digital Design and Computer Architecture, ARM Edition* Sarah Harris,David Harris,2015-04-09

Digital Design and Computer Architecture ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing an ARM processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader s understanding and retention of key concepts and techniques The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises Digital Design and Computer Architecture David Harris,Sarah Harris,2010-07-26

Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent

Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material

Digital Design and Computer Architecture, RISC-V Edition Sarah Harris,David Harris,2021-07-12 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems [Digital Design and Computer Architecture](#) David Money Harris,Sarah L. Harris,2015 **Digital Design and Computer Architecture** David Maney Harris,Sarah L. Harris,2007 **Digital Design And Computer Architecture** David Money Harris,2009-01-01 **Digital Logic** M. Rafiquzzaman,Steven A. McNinch,2019-09-11 Digital Logic with an Introduction to Verilog and FPGA Based Design provides basic knowledge of field programmable gate array FPGA design and implementation using Verilog a hardware description language HDL commonly used in the design and verification of digital circuits Emphasizing fundamental principles this student friendly textbook is an ideal resource for introductory digital logic courses Chapters offer clear explanations of key concepts and step by step procedures that illustrate the real world application of FPGA based design

Designed for beginning students familiar with DC circuits and the C programming language the text begins by describing of basic terminologies and essential concepts of digital integrated circuits using transistors Subsequent chapters cover device level and logic level design in detail including combinational and sequential circuits used in the design of microcontrollers and microprocessors Topics include Boolean algebra and functions analysis and design of sequential circuits using logic gates FPGA based implementation using CAD software tools and combinational logic design using various HDLs with focus on Verilog

Principles of Verilog Digital Design Wen-Long Chin, 2022-02-27 Covering both the fundamentals and the in depth topics related to Verilog digital design both students and experts can benefit from reading this book by gaining a comprehensive understanding of how modern electronic products are designed and implemented Principles of Verilog Digital Design contains many hands on examples accompanied by RTL codes that together can bring a beginner into the digital design realm without needing too much background in the subject area This book has a particular focus on how to transform design concepts into physical implementations using architecture and timing diagrams Common mistakes a beginner or even an experienced engineer can make are summarized and addressed as well Beyond the legal details of Verilog codes the book additionally presents what uses Verilog codes have through some pertinent design principles Moreover students reading this book will gain knowledge about system level design concepts Several ASIC designs are illustrated in detail as well In addition to design principles and skills modern design methodology and how it is carried out in practice today are explored in depth as well

The Electrical Engineering Handbook, Second Edition Richard C. Dorf, 1997-09-26 In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today Every electrical engineer should have an opportunity to expand his expertise with this definitive guide In a single volume this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry government or academia This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits signal processing electronics electromagnetics electrical effects and devices and energy and the emerging trends in the fields of communications digital devices computer engineering systems and biomedical engineering A compendium of physical chemical material and mathematical data completes this comprehensive resource Every major topic is thoroughly covered and every important concept is defined described and illustrated Conceptually challenging but carefully explained articles are equally valuable to the practicing engineer researchers and students A distinguished advisory board and contributors including many of the leading authors professors and researchers in the field today assist noted author and professor Richard Dorf in offering complete coverage of this rapidly expanding field No other single volume available today offers this combination of broad coverage and depth of exploration of the topics The Electrical Engineering Handbook will be an invaluable resource for electrical engineers for

years to come Computer Architecture and Organization: From 8085 to core2Duo & beyond Subrata Ghoshal,2011 The book uses microprocessors 8085 and above to explain the various concepts It not only covers the syllabi of most Indian universities but also provides additional information about the latest developments like Intel Core II Duo making it one of the most updated textbook in the market The book has an excellent pedagogy sections like food for thought and quicksand corner make for an interesting read **Digital Design and Computer Organisation** D. Nasib S. Gill,J.B. Dixit,2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles

Computer Organization and Design RISC-V Edition David A. Patterson,John L. Hennessy,2020-12-11 Computer Organization and Design RISC V Edition The Hardware Software Interface Second Edition the award winning textbook from Patterson and Hennessy that is used by more than 40 000 students per year continues to present the most comprehensive and readable introduction to this core computer science topic This version of the book features the RISC V open source instruction set architecture the first open source architecture designed for use in modern computing environments such as cloud computing mobile devices and other embedded systems Readers will enjoy an online companion website that provides advanced content for further study appendices glossary references links to software tools and more Covers parallelism in depth with examples and content highlighting parallel hardware and software topics Focuses on 64 bit address ISA to 32 bit address and ISA for RISC V because 32 bit RISC V ISA is simpler to explain and 32 bit address computers are still best for applications like embedded computing and IoT Includes new sections in each chapter on Domain Specific Architectures DSA Provides updates on all the real world examples in the book *Computer Architecture and Interfacing to Mechatronic Systems* Dario J. Toncich,1994 *ELECTRONICS* I. J. NAGRATH,2013-09-13 The second edition of this book has been updated and enlarged especially the chapters on digital electronics In the analog part several additions have been made wherever necessary Also optical devices and circuits have been introduced Analog electronics spans semiconductors diodes transistors small and large signal amplifiers OPAMPs and their applications Both BJT and JFET and MOSFET are treated parallelly so as to highlight their similarities and dissimilarities for thorough under standing of their parameters and specifications The digital electronics covers logic gates combinational circuits IC families number systems codes adders

subtractors flip flops registers and counters Sequential circuits memories and D A and A D convertor circuits are especially stressed Fabrication technology of integrated devices and circuits have also been dealt with Besides many new examples and problems have been added section wise The text is written in simple yet rigorous manner with profusion of illustrative examples as an aid to clear understanding The student can self study several portions of the book with minimal guidance A solution manual is available for the teachers

Computer Systems Ata Elahi,2022-03-16 This updated textbook covers digital design fundamentals of computer architecture and ARM assembly language The book starts by introducing computer abstraction basic number systems character coding basic knowledge in digital design and components of a computer The book goes on to discuss information representation in computing Boolean algebra and logic gates and sequential logic The book also presents introduction to computer architecture Cache mapping methods and virtual memory The author also covers ARM architecture ARM instructions ARM assembly language using Keil development tools and bitwise control structure using C and ARM assembly language The book includes a set of laboratory experiments related to digital design using Logisim software and ARM assembly language programming using Keil development tools In addition each chapter features objectives summaries key terms review questions and problems

[Digital Design Media](#) William J. Mitchell, Malcolm McCullough,1995 In Digital Design Media Second Edition architects and related design professionals will find a complete conceptual guide to the multidimensional world of computer aided design In contrast to the many books that describe how to use particular programs and which therefore go out of date very quickly Digital Design Media constructs a lasting theoretical framework which will make it easier to understand a great number of programs existing and future as a whole Clear structure numerous historical references and hundreds of illustrations make this framework both accessible to the nontechnical professional and broadening for the experienced computer aided designer The book will be especially valuable to anyone who is ready to expand their work in CAD beyond production drafting systems The new second edition adds chapters one merging technologies such as the Internet but the book s original content is as valid as ever Thousands of design students and practitioners have made this book a standard

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