

Jörg Henkel
Sri Parameswaram
Editors

Designing Embedded Processors

Designing Embedded Processors: Modules 13

A Low Power Perspective



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Designing Embedded Processors A Low Power Perspective

Vojin G. Oklobdzija



Designing Embedded Processors A Low Power Perspective:

Designing Embedded Processors Jörg Henkel, Sri Parameswaran, 2007-07-27 As we embrace the world of personal portable and perplexingly complex digital systems it has befallen upon the bewildered designer to take advantage of the available transistors to produce a system which is small fast cheap and correct yet possesses increased functionality Increasingly these systems have to consume little energy Designers are increasingly turning towards small processors which are low power and customize these processors both in software and hardware to achieve their objectives of a low power system which is verified and has short design turnaround times Designing Embedded Processors examines the many ways in which processor based systems are designed to allow low power devices It looks at processor design methods memory optimization dynamic voltage scaling methods compiler methods and multi processor methods Each section has an introductory chapter to give a breadth view and have a few specialist chapters in the area to give a deeper perspective The book provides a good starting point to engineers in the area and to research students embarking upon the exciting area of embedded systems and architectures

Ultra-Low Power Integrated Circuit Design Nianxiong Nick Tan, Dongmei Li, Zhihua Wang, 2013-10-23 This book describes the design of CMOS circuits for ultra low power consumption including analog radio frequency RF and digital signal processing circuits DSP The book addresses issues from circuit and system design to production design and applies the ultra low power circuits described to systems for digital hearing aids and capsule endoscope devices Provides a valuable introduction to ultra low power circuit design aimed at practicing design engineers Describes all key building blocks of ultra low power circuits from a systems perspective Applies circuits and systems described to real product examples such as hearing aids and capsule endoscopes

Design Principles for Embedded Systems KCS Murti, 2021-09-20 The book is designed to serve as a textbook for courses offered to graduate and undergraduate students enrolled in electronics and electrical engineering and computer science This book attempts to bridge the gap between electronics and computer science students providing complementary knowledge that is essential for designing an embedded system The book covers key concepts tailored for embedded system design in one place The topics covered in this book are models and architectures Executable Specific Languages SystemC Unified Modeling Language real time systems real time operating systems networked embedded systems Embedded Processor architectures and platforms that are secured and energy efficient A major segment of embedded systems needs hard real time requirements This textbook includes real time concepts including algorithms and real time operating system standards like POSIX threads Embedded systems are mostly distributed and networked for deterministic responses The book covers how to design networked embedded systems with appropriate protocols for real time requirements Each chapter contains 2 3 solved case studies and 10 real world problems as exercises to provide detailed coverage and essential pedagogical tools that make this an ideal textbook for students enrolled in electrical and electronics engineering and computer science programs

The

Green Computing Book Wu-chun Feng, 2014-06-16 State of the Art Approaches to Advance the Large Scale Green Computing Movement Edited by one of the founders and lead investigator of the Green500 list The Green Computing Book Tackling Energy Efficiency at Large Scale explores seminal research in large scale green computing It begins with low level hardware based approaches and then traverses up the software stack with increasingly higher level software based approaches In the first chapter the IBM Blue Gene team illustrates how to improve the energy efficiency of a supercomputer by an order of magnitude without any system performance loss in parallelizable applications The next few chapters explain how to enhance the energy efficiency of a large scale computing system via compiler directed energy optimizations an adaptive run time system and a general prediction performance framework The book then explores the interactions between energy management and reliability and describes storage system organization that maximizes energy efficiency and reliability It also addresses the need for coordinated power control across different layers and covers demand response policies in computing centers The final chapter assesses the impact of servers on data center costs

Energy-Efficient Distributed Computing Systems Albert Y. Zomaya, Young Choon Lee, 2012-07-26 The energy consumption issue in distributed computing systems raises various monetary environmental and system performance concerns Electricity consumption in the US doubled from 2000 to 2005 From a financial and environmental standpoint reducing the consumption of electricity is important yet these reforms must not lead to performance degradation of the computing systems These contradicting constraints create a suite of complex problems that need to be resolved in order to lead to greener distributed computing systems This book brings together a group of outstanding researchers that investigate the different facets of green and energy efficient distributed computing Key features One of the first books of its kind Features latest research findings on emerging topics by well known scientists Valuable research for grad students postdocs and researchers Research will greatly feed into other technologies and application domains

Circuits and Systems for the Internet of Things João Goes, 2022-09-01 Internet of Things IoT can be envisaged as a dynamic network of interconnected physical and virtual entities things with their own identities and attributes seamlessly integrated in order to e g actively participate in economic or societal processes interact with services and react autonomously to events while sensing the environment By enabling things to connect and becoming recognizable while providing them with intelligence informed and context based decisions are expected in a broad range of domains spanning from health and elderly care to energy efficiency either providing business competitive advantages to companies either addressing key social concerns The level of connectivity and analytical intelligence provided by the IoT paradigm is expected to allow creating new services that would not be feasible by other means This CAS4IoT book targets post graduate students and design engineers with the skills to understand and design a broader range of analog digital and mixed signal circuits and systems in the field of IoT spanning from data converters for sensor interfaces to radios ensuring a good balance between academia and industry combined with a judicious selection of worldwide distinguished authors

Neuromorphic Computing and Beyond Khaled Salah Mohamed, 2020-01-25 This book discusses and compares several new trends that can be used to overcome Moore's law limitations including Neuromorphic Approximate Parallel In Memory and Quantum Computing The author shows how these paradigms are used to enhance computing capability as developers face the practical and physical limitations of scaling while the demand for computing power keeps increasing The discussion includes a state of the art overview and the essential details of each of these paradigms

Handbook of Energy-Aware and Green Computing, Volume 2 Ishfaq Ahmad, Sanjay Ranka, 2013-01-31 This book provides basic and fundamental knowledge of various aspects of energy aware computing at the component software and system level It provides a broad range of topics dealing with power energy and temperature related research areas for individuals from industry and academia

Advances in Parallel, Distributed Computing Dhinaharan Nagamalai, Eric Renault, Murugan Dhanuskodi, 2011-10-12 This book constitutes the refereed proceedings of the First International Conference on Advances in Parallel Distributed Computing Technologies and Applications PDCTA 2011 held in Tirunelveli India in September 2011 The 64 revised full papers were carefully reviewed and selected from over 400 submissions Providing an excellent international forum for sharing knowledge and results in theory methodology and applications of parallel distributed computing the papers address all current issues in this field with special focus on algorithms and applications computer networks cyber trust and security wireless networks as well as mobile computing and bioinformatics

Pipelined Multiprocessor System-on-Chip for Multimedia Haris Javaid, Sri Parameswaran, 2013-11-26 This book describes analytical models and estimation methods to enhance performance estimation of pipelined multiprocessor systems on chip MPSoCs A framework is introduced for both design time and run time optimizations For design space exploration several algorithms are presented to minimize the area footprint of a pipelined MPSoC under a latency or a throughput constraint A novel adaptive pipelined MPSoC architecture is described where idle processors are transitioned into low power states at run time to reduce energy consumption Multi mode pipelined MPSoCs are introduced where multiple pipelined MPSoCs optimized separately are merged into a single pipelined MPSoC enabling further reduction of the area footprint by sharing the processors and communication buffers Readers will benefit from the authors combined use of analytical models estimation methods and exploration algorithms and will be enabled to explore billions of design points in a few minutes

Computers as Components Marilyn Wolf, 2016-09-12 Computers as Components Principles of Embedded Computing System Design Fourth Edition continues to focus on foundational content in embedded systems technology and design while introducing new content on security and safety the design of Internet of Things devices and systems and wireless communications standards like Bluetooth and ZigBee Uses real processors to demonstrate both technology and techniques Shows readers how to apply principles to actual design practice Stresses necessary fundamentals that can be applied to evolving technologies and helps readers gain facility to design large complex embedded systems Covers the design of Internet of Things IoT devices and systems including applications devices

and communication systems and databases Introduces concepts of safety and security in embedded systems Includes new chapter on Automotive and Aerospace Systems Describes wireless communication standards such as Bluetooth and ZigBee

Design of Energy-Efficient Application-Specific Instruction Set Processors Tilman Glökler, Heinrich

Meyr, 2007-05-08 After a brief introduction to low power VLSI design the design space of ASIP instruction set architectures ISAs is introduced with a special focus on important features for digital signal processing Based on the degrees of freedom offered by this design space a consistent ASIP design flow is proposed this design flow starts with a given application and uses incremental optimization of the ASIP hardware of ASIP coprocessors and of the ASIP software by using a top down approach and by applying application specific modifications on all levels of design hierarchy A broad range of real world signal processing applications serves as vehicle to illustrate each design decision and provides a hands on approach to ASIP design Finally two complete case studies demonstrate the feasibility and the efficiency of the proposed methodology and quantitatively evaluate the benefits of ASIPs in an industrial context

Data Access and Storage Management for Embedded Programmable Processors Francky Catthoor, K. Danckaert, K.K. Kulkarni, E. Brockmeyer, Per Gunnar Kjeldsberg, T. van Achteren, Thierry Omnes, 2013-03-14 Data Access and Storage Management for Embedded Programmable Processors gives an overview of the state of the art in system level data access and storage management for embedded programmable processors The targeted application domain covers complex embedded real time multi media and communication applications Many of these applications are data dominated in the sense that their cost related aspects namely power consumption and footprint are heavily influenced if not dominated by the data access and storage aspects The material is mainly based on research at IMEC in this area in the period 1996 2001 In order to deal with the stringent timing requirements and the data dominated characteristics of this domain we have adopted a target architecture style that is compatible with modern embedded processors and we have developed a systematic step wise methodology to make the exploration and optimization of such applications feasible in a source to source precompilation approach

Unified low-power design flow for data-dominated multi-media and telecom applications Francky Catthoor, 2013-03-14 This book is the first in a series on novel low power design architectures methods and design practices It results from a large European project started in 1997 whose goal is to promote the further development and the faster and wider industrial use of advanced design methods for reducing the power consumption of electronic systems Low power design became crucial with the wide spread of portable information and communication terminals where a small battery has to last for a long period High performance electronics in addition suffers from a permanent increase of the dissipated power per square millimetre of silicon due to the increasing clock rates which causes cooling and reliability problems or otherwise limits the performance The European Union's Information Technologies Programme Esprit did therefore launch a Pilot action for Low Power Design which eventually grew to 19 R D projects and one coordination project with an overall budget of 14 million Euro It is

meanwhile known as European Low Power Initiative for Electronic System Design ESD LPD and will be completed by the end of 2001 It involves 30 major European companies and 20 well known institutes The R D projects aims to develop or demonstrate new design methods for power reduction while the coordination project takes care that the methods experiences and results are properly documented and published Low Power Design in Deep Submicron Electronics W. Nebel, Jean Mermet, 2013-06-29 Low Power Design in Deep Submicron Electronics deals with the different aspects of low power design for deep submicron electronics at all levels of abstraction from system level to circuit level and technology Its objective is to guide industrial and academic engineers and researchers in the selection of methods technologies and tools and to provide a baseline for further developments Furthermore the book has been written to serve as a textbook for postgraduate student courses In order to achieve both goals it is structured into different chapters each of which addresses a different phase of the design a particular level of abstraction a unique design style or technology These design related chapters are amended by motivations in Chapter 2 which presents visions both of future low power applications and technology advancements and by some advanced case studies in Chapter 9 From the Foreword This global nature of design for low power was well understood by Wolfgang Nebel and Jean Mermet when organizing the NATO workshop which is the origin of the book They invited the best experts in the field to cover all aspects of low power design As a result the chapters in this book are covering deep submicron CMOS digital system design for low power in a systematic way from process technology all the way up to software design and embedded software systems Low Power Design in Deep Submicron Electronics is an excellent guide for the practicing engineer the researcher and the student interested in this crucial aspect of actual CMOS design It contains about a thousand references to all aspects of the recent five years of feverish activity in this exciting aspect of design Hugo de Man Professor K U Leuven Belgium Senior Research Fellow IMEC Belgium

Low-Power Processors and Systems on Chips Christian Pigué, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Pigué's recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and verification tools Providing detailed examinations contributed by leading

experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems

Mastering High-speed and Low Power Hardware System Design Mr. Kush Desai , Ms. Khushbu Shah,2025-03-11 Mastering High speed and Low Power Hardware System Design is an authoritative guide that delves into the intricate world of high speed and low power hardware design offering comprehensive insights into the principles techniques and best practices that drive the development of efficient high performance hardware systems This book addresses the critical challenges of optimizing hardware systems for both speed and power consumption two fundamental aspects that are increasingly important in the design of modern electronic systems Beginning with an introduction to the basic concepts of digital systems signal integrity and power consumption this book progressively moves into more advanced topics such as high frequency signal design clock management power efficient circuit design and the integration of cutting edge technologies It explores the trade offs and strategies involved in achieving the ideal balance between system speed and energy efficiency with a focus on real world applications in industries such as telecommunications consumer electronics automotive and embedded systems Through clear explanations practical examples and case studies the book empowers engineers students and professionals to master the complexities of hardware design while fostering an understanding of how to create systems that are both high speed and low power Whether designing communication systems processors or integrated circuits Mastering High speed and Low Power Hardware System Design provides the knowledge and tools needed to push the boundaries of hardware performance while adhering to stringent power consumption requirements With contributions from leading experts in the field this book offers invaluable insights into the future of hardware system design making it an essential resource for anyone looking to excel in the design and optimization of advanced hardware systems in today s rapidly evolving technological landscape

Digital Design and Fabrication Vojin G. Oklobdzija,2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user s point of view Discusses design techniques used to create modern computer systems including

high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Nadine Azemard, 2007-08-21 This volume features the refereed proceedings of the 17th International Workshop on Power and Timing Modeling Optimization and Simulation Papers cover high level design low power design techniques low power analog circuits statistical static timing analysis power modeling and optimization low power routing optimization security and asynchronous design low power applications modeling and optimization and more The Electrical Engineering Handbook Wai Kai Chen, 2004-11-16 The Electrical Engineer s Handbook is an invaluable reference source for all practicing electrical engineers and students Encompassing 79 chapters this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students This text will most likely be the engineer s first choice in looking for a solution extensive complete references to other sources are provided throughout No other book has the breadth and depth of coverage available here This is a must have for all practitioners and students The Electrical Engineer s Handbook provides the most up to date information in Circuits and Networks Electric Power Systems Electronics Computer Aided Design and Optimization VLSI Systems Signal Processing Digital Systems and Computer Engineering Digital Communication and Communication Networks Electromagnetics and Control and Systems About the Editor in Chief Wai Kai Chen is Professor and Head Emeritus of the Department of Electrical Engineering and Computer Science at the University of Illinois at Chicago He has extensive experience in education and industry and is very active professionally in the fields of circuits and systems He was Editor in Chief of the IEEE Transactions on Circuits and Systems Series I and II President of the IEEE Circuits and Systems Society and is the Founding Editor and Editor in Chief of the Journal of Circuits Systems and Computers He is the recipient of the Golden Jubilee Medal the Education Award and the Meritorious Service Award from the IEEE Circuits and Systems Society and the Third Millennium Medal from the IEEE Professor Chen is a fellow of the IEEE and the American Association for the Advancement of Science 77 chapters encompass the entire field of electrical engineering THOUSANDS of valuable figures tables formulas and definitions Extensive bibliographic references

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