

# **Dependability Benchmarking For Computer Systems**

**JR Anderson** 

## **Dependability Benchmarking For Computer Systems:**

Dependability Benchmarking for Computer Systems Karama Kanoun, Lisa Spainhower, 2008-10-03 A comprehensive collection of benchmarks for measuring dependability in hardware software systems As computer systems have become more complex and mission critical it is imperative for systems engineers and researchers to have metrics for a system s dependability reliability availability and serviceability Dependability benchmarks are useful for guiding development efforts for system providers acquisition choices of system purchasers and evaluations of new concepts by researchers in academia and industry This book gathers together all dependability benchmarks developed to date by industry and academia and explains the various principles and concepts of dependability benchmarking It collects the expert knowledge of DBench a research project funded by the European Union and the IFIP Special Interest Group on Dependability Benchmarking to shed light on this important area It also provides a large panorama of examples and recommendations for defining dependability benchmarks Dependability Benchmarking for Computer Systems includes contributions from a credible mix of industrial and academic sources IBM Intel Microsoft Sun Microsystems Critical Software Carnegie Mellon University LAAS CNRS Technical University of Valencia University of Coimbra and University of Illinois It is an invaluable resource for engineers researchers system vendors system purchasers computer industry consultants and system integrators Advances in Computers Ali R. Hurson, Sahra Sedigh, 2012-04-18 Since its first volume in 1960 Advances in Computers has presented detailed coverage of innovations in computer hardware software theory design and applications It has also provided contributors with a medium in which they can explore their subjects in greater depth and breadth than journal articles usually allow As a result many articles have become standard references that continue to be of sugnificant lasting value in this rapidly expanding field In depth surveys and tutorials on new computer technology Well known authors and researchers in the field Extensive bibliographies with most chapters Many of the volumes are devoted to single themes or subfields of computer science

Computer Safety, Reliability, and Security Maritta Heisel, Peter Liggesmeyer, Stefan Wittmann, 2004-10-29
Theimportanceofsafetyandsecurity is growing steadily Safety is aquality cracteristic that traditionally has been considered to be important in embedded systems and security is usually an essential property in business applications. There is certainly a tendency to use software based solutions in safety critical applications domains which increases the importance of safety engineering te niques. These include modelling and analysis techniques as well as appropriate processes and tools. And it is surely correct that the amount of con dential data that require protection from unauthorized access is growing. Therefore security is very important. On the one hand the traditional motivations for addressing safety and security still exist and their relevance has improved. On the other hand safety and security requirements occur increasingly in the same system. At present many software based systems interact with technical equipment and they communicate e.g. with users and other systems. Future systems will more and more interact with many other entities technical systems people the en ronment. In this

situation security problems may cause safety related failures It is thus necessary to address safety and security It is furthermore required to take into account the interactions between these two properties Integrated Research in GRID Computing Sergei Gorlatch, Marco Danelutto, 2007-04-30 The aim of CoreGRID is to strengthen and advance scientific and technological excellence in the area of Grid and Peer to Peer technologies in order to overcome the current fragmentation and duplication of effort in this area To achieve this objective the workshop brought together a critical mass of well established researchers from a number of institutions which have all constructed an ambitious joint program of activities Priority in the workshop was given to work conducted in collaboration between partners from different research institutions and to promising research proposals that could foster such collaboration in the future **Performance Evaluation and Benchmarking** Raghunath Nambiar, Meikel Poess, 2009-10-27 First established in August 1988 the Transaction Processing Performance Council TPC has shaped the landscape of modern transaction processing and database benchmarks over two decades Now the world is in the midst of an extraordinary information explosion led by rapid growth in the use of the Internet and connected devices Both user generated data and enterprise data levels continue to grow ex nentially With substantial technological breakthroughs Moore's law will continue for at least a decade and the data storage capacities and data transfer speeds will continue to increase exponentially These have challenged industry experts and researchers to develop innovative techniques to evaluate and benchmark both hardware and software technologies As a result the TPC held its First Conference on Performance Evaluation and Benchmarking TPCTC 2009 on August 24 in Lyon France in conjunction with the 35th International Conference on Very Large Data Bases VLDB 2009 TPCTC 2009 provided industry experts and researchers with a forum to present and debate novel ideas and methodologies in performance evaluation measurement and characteri tion for 2010 and beyond This book contains the proceedings of this conference including 16 papers and keynote papers from Michael Stonebraker and Karl Huppler Reliability Engineering Alessandro Birolini, 2017-05-19 This book shows how to build in and assess reliability availability maintainability and safety RAMS of components equipment and systems It presents the state of the art of reliability RAMS engineering in theory practice and is based on over 30 years author's experience in this field half in industry and half as Professor of Reliability Engineering at the ETH Zurich The book structure allows rapid access to practical results Methods tools are given in a way that they can be tailored to cover different RAMS requirement levels Thanks to Appendices A6 A8 the book is mathematically self contained and can be used as a textbook or as a desktop reference with a large number of tables 60 figures 210 and examples exercises 10 000 per year since 2013 were the motivation for this final edition the 13th since 1985 including German editions Extended and carefully reviewed to improve accuracy it represents the continuous improvement effort to satisfy reader s needs and confidence New are an introduction to risk management with structurally new models based on semi Markov processes to the concept of mean time to accident reliability availability of a k out of n redundancy with arbitrary repair rate for n k 2 10 new homework

problems and refinements in particular on multiple failure mechanisms approximate expressions incomplete coverage data analysis and comments on MTBF MTTF MTTR R PA **Dependable Computing Systems** Hassan B. Diab, Albert Y. Zomaya, 2005-10-05 A team of recognized experts leads the way to dependable computing systems With computers and networks pervading every aspect of daily life there is an ever growing demand for dependability In this unique resource researchers and organizations will find the tools needed to identify and engage state of the art approaches used for the specification design and assessment of dependable computer systems. The first part of the book addresses models and paradigms of dependable computing and the second part deals with enabling technologies and applications Tough issues in creating dependable computing systems are also tackled including Verification techniques Model based evaluation Adjudication and data fusion Robust communications primitives Fault tolerance Middleware Grid security Dependability in IBM mainframes Embedded software Real time systems Each chapter of this contributed work has been authored by a recognized expert This is an excellent textbook for graduate and advanced undergraduate students in electrical engineering computer engineering and computer science as well as a must have reference that will help engineers programmers and technologists develop systems that are secure and reliable **Achievements in European Research on Grid Systems** Sergei Gorlatch, Marian Bubak, Thierry Priol, 2007-10-05 This volume comprises the edited proceedings of the second CoreGRID Integration Workshop CGIW 2006 held October 2006 in Krakow Poland A Network of Excellence funded by the European Commission's Sixth Framework Program CoreGRID aims to strengthen and advance scientific and technological excellence in the area of Grid and Peer to Peer technologies by bringing together a critical mass of well established researchers from 41 European research institutions Designed for a professional audience of industry practitioners and researchers the volume is also suitable for advanced level students in computer science Advances in Computer Science and Ubiquitous Computing James J. Park, Vincenzo Loia, Gangman Yi, Yunsick Sung, 2017-12-19 This book presents the combined proceedings of the 12th KIPS International Conference on Ubiquitous Information Technologies and Applications CUTE 2017 and the 9th International Conference on Computer Science and its Applications CSA2017 both held in Taichung Taiwan December 18 20 2017 The aim of these two meetings was to promote discussion and interaction among academics researchers and professionals in the field of ubiquitous computing technologies. These proceedings reflect the state of the art in the development of computational methods involving theory algorithms numerical simulation error and uncertainty analysis and novel applications of new processing techniques in engineering science and other disciplines related to ubiquitous computing James J Jong Hyuk Park received Ph D degrees in Graduate School of Information Security from Korea University Korea and Graduate School of Human Sciences from Waseda University Japan From December 2002 to July 2007 Dr Park had been a research scientist of R D Institute Hanwha S C Co Ltd Korea From September 2007 to August 2009 He had been a professor at the Department of Computer Science and Engineering Kyungnam University Korea He is now a

professor at the Department of Computer Science and Engineering and Department of Interdisciplinary Bio IT Materials Seoul National University of Science and Technology SeoulTech Korea Dr Park has published about 200 research papers in international journals and conferences He has been serving as chair program committee or organizing committee chair for many international conferences and workshops He is a steering chair of international conferences MUE FutureTech CSA CUTE UCAWSN World IT Congress Jeju He is editor in chief of Human centric Computing and Information Sciences HCIS by Springer The Journal of Information Processing Systems JIPS by KIPS and Journal of Convergence JoC by KIPS CSWRG He is Associate Editor Editor of 14 international journals including JoS JNCA SCN CI and so on In addition he has been serving as a Guest Editor for international journals by some publishers Springer Elsevier John Wiley Oxford Univ press Emerald Inderscience MDPI He got the best paper awards from ISA 08 and ITCS 11 conferences and the outstanding leadership awards from IEEE HPCC 09 ICA3PP 10 IEE ISPA 11 PDCAT 11 IEEE AINA 15 Furthermore he got the outstanding research awards from the SeoulTech 2014 His research interests include IoT Human centric Ubiquitous Computing Information Security Digital Forensics Vehicular Cloud Computing Multimedia Computing etc He is a member of the IEEE IEEE Computer Society KIPS and KMMS Vincenzo Loia BS 85 MS 87 PhD 89 is Full Professor of Computer Science His research interests include Intelligent Agents Ambient intelligence Computational Intelligence Currently he is Founder Editor in chief of Ambient Intelligence and Humanized Computing and Co Editor in Chief of Softcomputing Springer Verlag He is Chair of the Task Forces Intelligent Agents and Ambient Intelligence IEEE CIS ETTC He has been Chair the Emergent Technical Committe Emergent Technology IEEE CIS Society and Vice Chair of Intelligent Systems Applications Technical Committee He has been author of more than 200 scientific works Editor co editor of 4 Books 64 journal papers 25 book chapters and 100 conference papers He is Senior member of the IEEE Associate Editor of IEEE Transactions on Industrial Informatics and Associate Editor of IEEE Transactions on Systems Man and Cybernetics Systems Many times reviewers for national and international projects Dr Loia is active in the research domain of agents ambient intelligence computational intelligence smartgrids distributed platform for enrich added value Gangman Yi in Computer Sciences at Texas A M University USA in 2007 and doctorate in Computer Sciences at Texas A M University USA in 2011 In May 2011 he joined System S W group in Samsung Electronics Suwon Korea He joined the Department of Computer Science Engineering Gangneung Wonju National University Korea since March 2012 Dr Yi has been researched in an interdisciplinary field of researches His research focuses especially on the development of computational methods to improve understanding of biological systems and its big data Dr Yi actively serves as a managing editor and reviewer for international journals and chair of international conferences and workshops Yunsick Sung received his B S degree in division of electrical and computer engineering from Pusan National University Busan Korea in 2004 his M S degree in computer engineering from Dongguk University Seoul Korea in 2006 and his Ph D degree in game engineering from Dongguk University Seoul Korea in 2012 He was employed as a member of the

researcher at Samsung Electronics between 2006 and 2009 He was the plural professor at Shinheung College in 2009 and at Dongguk University in 2010 His main research interests are many topics in brain computer Interface programming by demonstration ubiquitous computing and reinforcement learning His Journal Service Experiences is Associate Editor at Human centric Computing and Information Sciences Springer 2015 Current Risk, Reliability and Safety: Innovating **Theory and Practice** Lesley Walls, Matthew Revie, Tim Bedford, 2016-11-25 The safe and reliable performance of many systems with which we interact daily has been achieved through the analysis and management of risk From complex infrastructures to consumer durables from engineering systems and technologies used in transportation health energy chemical oil gas aerospace maritime defence and other sectors the management of risk during design manufacture operation and decommissioning is vital Methods and models to support risk informed decision making are well established but are continually challenged by technology innovations increasing interdependencies and changes in societal expectations Risk Reliability and Safety contains papers describing innovations in theory and practice contributed to the scientific programme of the European Safety and Reliability conference ESREL 2016 held at the University of Strathclyde in Glasgow Scotland 25 29 September 2016 Authors include scientists academics practitioners regulators and other key individuals with expertise and experience relevant to specific areas Papers include domain specific applications as well as general modelling methods Papers cover evaluation of contemporary solutions exploration of future challenges and exposition of concepts methods and processes Topics include human factors occupational health and safety dynamic and systems reliability modelling maintenance optimisation uncertainty analysis resilience assessment risk and crisis management Resilience Assessment and Evaluation of Computing Systems Katinka Wolter, Alberto Avritzer, Marco Vieira, Aad van Moorsel, 2012-11-02 The resilience of computing systems includes their dependability as well as their fault tolerance and security It defines the ability of a computing system to perform properly in the presence of various kinds of disturbances and to recover from any service degradation These properties are immensely important in a world where many aspects of our daily life depend on the correct reliable and secure operation of often large scale distributed computing systems Wolter and her co editors grouped the 20 chapters from leading researchers into seven parts an introduction and motivating examples modeling techniques model driven prediction measurement and metrics testing techniques case studies and conclusions The core is formed by 12 technical papers which are framed by motivating real world examples and case studies thus illustrating the necessity and the application of the presented methods While the technical chapters are independent of each other and can be read in any order the reader will benefit more from the case studies if he or she reads them together with the related techniques The papers combine topics like modeling benchmarking testing performance evaluation and dependability and aim at academic and industrial researchers in these areas as well as graduate students and lecturers in related fields In this volume they will find a comprehensive overview of the state of the art in a field of continuously growing practical importance

Advances in Dependability Engineering of Complex Systems Wojciech Zamojski, Jacek Mazurkiewicz, Jarosław Sugier, Tomasz Walkowiak, Janusz Kacprzyk, 2017-05-29 This book gathers the proceedings of the 2017 DepCoS RELCOMEX an annual conference series that has been organized by the Department of Computer Engineering at the Faculty of Electronics Wroc aw University of Science and Technology since 2006 Its mission is to continue the heritage of the other two cycles of events the RELCOMEX conferences 1977 89 and Microcomputer Schools 1985 95 so this year we can celebrate the 40th anniversary of its origins In contrast to those preceding series which were focused on conventional reliability analysis the goal of DepCoS is to promote a more comprehensive approach to system performability which is now commonly called dependability. This innovative research area provides answers to the latest challenges in reliability evaluation for contemporary complex systems Its novelty is based on a multi disciplinary approach to system theory technology and maintenance of systems operating in real environments Dependability analyses concentrate on the efficient completion of tasks services and jobs by a system considered as a combination of technical information and human assets in contrast to classical reliability which is generally limited to the analysis of technical resources and associated components and structures The selection of papers for this volume illustrates the diversity of topics that need to be considered from mathematical models and design methodologies through software engineering and data security issues to practical engineering problems in technical systems In addition this edition of the conference hosted the 7th CrISS DESSERT Workshop which was devoted to the analysis and assurance of safety and cyber security in critical infrastructure and computer systems **Electronic Commerce, Web Application and Communication** David Jin, Sally Lin, 2012-02-24 ECWAC2012 is an integrated conference devoted to Electronic Commerce Web Application and Communication In the this proceedings you can find the carefully reviewed scientific outcome of the second International Conference on Electronic Commerce Web Application and Communication ECWAC 2012 held at March 17 18 2012 in Wuhan China bringing together researchers from all around the world in the field System Dependability and Analytics Long Wang, Karthik Pattabiraman, Catello Di Martino, Arjun Athreya, Saurabh Bagchi, 2022-07-25 This book comprises chapters authored by experts who are professors and researchers in internationally recognized universities and research institutions. The book presents the results of research and descriptions of real world systems services and technologies Reading this book researchers professional practitioners and graduate students will gain a clear vision on the state of the art of the research and real world practice on system dependability and analytics The book is published in honor of Professor Ravishankar K Iyer the George and Ann Fisher Distinguished Professor in the Department of Electrical and Computer Engineering at the University of Illinois at Urbana Champaign UIUC Urbana Illinois Professor Iyer is ACM Fellow IEEE Fellow AAAS Fellow and served as Interim Vice Chancellor of UIUC for research during 2008 2011 The book contains chapters written by many of his former students Dependability Metrics Irene Eusgeld, Felix Freiling, Ralf H. Reussner, 2008-05-30 This tutorial book gives an overview of

the current state of the art in measuring the different aspects of dependability of systems reliability security and Topics in Performance Evaluation, Measurement and Characterization Raghunath Nambiar, Meikel performance Poess, 2012-08-04 This book constitutes the proceedings of the Third Technology Conference on Performance Evaluation and Benchmarking TPCTC 2011 held in conjunction with the 37th International Conference on Very Large Data Bases VLDB 2011 in Seattle August September 2011 The 12 full papers and 2 keynote papers were carefully selected and reviewed from numerous submissions. The papers present novel ideas and methodologies in performance evaluation measurement and Architectina Dependable Systems VI Rogério de Lemos, Jean-Charles Fabre, Cristina Gacek, Fabio Gadducci, Maurice H. ter Beek, 2009-10-27 As software systems become increasingly ubiquitous issues of dependability become ever more crucial Given that solutions to these issues must be considered from the very beginning of the design process it is reasonable that dependability and security are addressed at the architectural level This book has originated from an effort to bring together the research communities of software architectures dependability and security This state of the art survey contains expanded and peer reviewed papers based on the carefully selected contributions to two workshops the Workshop on Architecting Dependable Systems WADS 2008 organized at the 2008 International Conference on Dependable Systems and Networks DSN 2008 held in Anchorage Alaska USA in June 2008 and the Third International Workshop on Views On Designing Complex Architectures VODCA 2008 held in Bertinoro Italy in August 2008 It also contains invited papers written by recognized experts in the area The 13 papers are organized in topical sections on dependable service oriented architectures fault tolerance and system evaluation and architecting security **Innovative Technologies for Dependable OTS-Based Critical Systems** Domenico Cotroneo, 2013-01-24 The demand for large scale dependable systems such as Air Traffic Management industrial plants and space systems is attracting efforts of many word leading European companies and SMEs in the area and is expected to increase in the near future The adoption of Off The Shelf OTS items plays a key role in such a scenario OTS items allow mastering complexity and reducing costs and time to market however achieving these goals by ensuring dependability requirements at the same time is challenging CRITICAL STEP project establishes a strategic collaboration between academic and industrial partners and proposes a framework to support the development of dependable OTS based critical systems The book introduces methods and tools adopted by the critical systems industry and surveys key achievements of the CRITICAL STEP project along four directions fault injection tools V V of critical systems runtime monitoring and evaluation techniques and security assessment Models in Hardware Testing Hans-Joachim Wunderlich, 2009-11-12 Model based testing is the most powerful technique for testing hardware and software systems Models in Hardware Testing describes the use of models at all the levels of hardware testing The relevant fault models for nanoscaled CMOS technology are introduced and their implications on fault simulation automatic test pattern generation fault diagnosis memory testing and power aware testing are discussed Models and the corresponding algorithms

are considered with respect to the most recent state of the art and they are put into a historical context by a concluding chapter on the use of physical fault models in fault tolerance *Dependable Computing* Rogério le Lemos, Taisy Silva Weber, Joao Batista Camargo Jr., 2003-10-02 This book constitutes the refereed proceedings of the First Latin American Symposium on Dependable Computing LADC 2003 held in Sao Paulo Brazil in October 2003 The 21 revised full papers presented together with abstracts of invited talks a panel workshops and tutorials were carefully reviewed and selected for presentation The papers are organized in topical sections on fault injection security adaptive fault tolerance distributed algorithms and components and fault tolerance

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