#### Ringue eliment. Note 5.

#### ALC: NO

Verify the operation of a differentiator circuit using up amp 741,

### Apparatus Required:

C900, Function Generator, and 741 IC, x12V supply. Resistors, Capacitors and Connecting leads.

### Theorem :

Differentiator circuits as its more implies, performs the mathematical operation of differentiator, that is, the output inavellarm is the derivative of the irgut. The differentiator may be constructed from a basic inverting amplifier when an irgut resistor R1 is replaced by a capacitor C.

Thus, the output Vo is equal to the REC times the negative instantaneous rate of change of the input voltage. Vin with time.

The right-hand side of the capacitor is held to a voltage of 0 volts, due to the "virtual ground" effect. Therefore, current "through" the capacitor is solely due to change in the legat voltage. A steady input voltage work cause a current through C, but a changing legat voltage will. Capacitor current revives through the foodback resistor, producing a drop across k, which is the same as the output voltage. A linear, positive rate of sput voltage change will result in a steady negative voltage at the output of the op-array. Conservely, a linear, regative rate of input voltage change will result in a steady positive voltage at the output of the op-array. This policity inversion from input to output is due to the fact that the input signal is being sent (essentially) to the inverting input of the op-array, so it acts like the inverting amplifier mentioned previously. The finite the rate of voltage change at the input settler positive or negative), the greater the soltage at the output. The finitesials for determining voltage output for the differentiator is an follows:

## Circuit Diagrams



#### Princip dainer.

- 1) Correct the circuit as per the circuit diagram,
- 2) Apply the input signals i.e. Sinnoidal, Square waves.
- 3) Check the output waveform i.e. the wave shape of the signal.
- 4) Note that the output will be the differentiation of the applied signal or not.
- 5) Plot various input output wave shapes.

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Jianjun Gao

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