

DRAM

CIRCUIT DESIGN

Fundamental and High-Speed Topics



Brent Keeth • R. Jacob Baker
Brian Johnson • Feng Lin

IEEE Press Series on Semiconductor Systems
Robert W. Truitt and John E. Droney, Series Editors

Dram Circuit Design Fundamentals And High Speed Topics 2nd Second Edition

Tertulien Ndjountche



Dram Circuit Design Fundamentals And High Speed Topics 2nd Second Edition:

DRAM Circuit Design Brent Keeth, R. Jacob Baker, Brian Johnson, Feng Lin, 2007-12-04 A modern comprehensive introduction to DRAM for students and practicing chip designers Dynamic Random Access Memory DRAM technology has been one of the greatest driving forces in the advancement of solid state technology With its ability to produce high product volumes and low pricing it forces solid state memory manufacturers to work aggressively to cut costs while maintaining if not increasing their market share As a result the state of the art continues to advance owing to the tremendous pressure to get more memory chips from each silicon wafer primarily through process scaling and clever design From a team of engineers working in memory circuit design *DRAM Circuit Design* gives students and practicing chip designers an easy to follow yet thorough introductory treatment of the subject Focusing on the chip designer rather than the end user this volume offers expanded up to date coverage of DRAM circuit design by presenting both standard and high speed implementations Additionally it explores a range of topics the DRAM array peripheral circuitry global circuitry and considerations voltage converters synchronization in DRAMs data path design and power delivery Additionally this up to date and comprehensive book features topics in high speed design and architecture and the ever increasing speed requirements of memory circuits The only book that covers the breadth and scope of the subject under one cover *DRAM Circuit Design* is an invaluable introduction for students in courses on memory circuit design or advanced digital courses in VLSI or CMOS circuit design It also serves as an essential one stop resource for academics researchers and practicing engineers

DRAM Circuit Design
Brent Keeth, R. Jacob Baker, 2001 *DRAM Circuit Design* teaches readers the introductory level design of DRAM memory chips It focuses on giving readers a reference that can be used to educate students or practicing design engineers in DRAM circuit design

Design of High-speed Communication Circuits Ramesh Harjani, 2006 MOS technology has rapidly become the de facto standard for mixed signal integrated circuit design due to the high levels of integration possible as device geometries shrink to nanometer scales The reduction in feature size means that the number of transistor and clock speeds have increased significantly In fact current day microprocessors contain hundreds of millions of transistors operating at multiple gigahertz Furthermore this reduction in feature size also has a significant impact on mixed signal circuits Due to the higher levels of integration the majority of ASICs possesses some analog components It has now become nearly mandatory to integrate both analog and digital circuits on the same substrate due to cost and power constraints This book presents some of the newer problems and opportunities offered by the small device geometries and the high levels of integration that is now possible The aim of this book is to summarize some of the most critical aspects of high speed analog RF communications circuits Attention is focused on the impact of scaling substrate noise data converters RF and wireless communication circuits and wireline communication circuits including high speed I/O Contents Achieving Analog Accuracy in Nanometer CMOS M. P. Flynn et al Self Induced Noise in Integrated Circuits R. Gharpurey High Speed Oversampling Analog to Digital Converters A

Gharbiya et al Designing LC VCOs Using Capacitive Degeneration Techniques B Jung Fully Integrated Frequency Synthesizers A Tutorial S T Moon et al Recent Advances and Design Trends in CMOS Radio Frequency Integrated Circuits D J Allstot et al Equalizers for High Speed Serial Links P K Hanumolu et al Low Power Parallel Interface with Continuous Time Adaptive Passive Equalizer and Crosstalk Cancellation C P Yue et al Readership Technologists scientists and engineers in the field of high speed communication circuits It can also be used as a textbook for graduate and advanced undergraduate courses

VLSI Memory Chip Design Kiyoo Itoh, 2013-04-17 The VLSI memory era truly began when the first production of semiconductor memory was announced by IBM and Intel in 1970 The announcement had a profound impact on my research at Hitachi Ltd and I was forced to change fields from magnetic thin film to semiconductor memory This change was so exceptionally sudden and difficult I felt like a victim of fate Looking back however I realize how fortunate I was I have witnessed an unprecedented increase in memory capacity DRAM for example has had a 6 order increase in the last three decades from the 1 Kb level in 1970 to the 1 Gb level today I have contributed to this progress with full involvement in memory chip development over my career Such rapid progress would have been impossible without many of the inventions and innovative technologies and without the effort of many talented people Unfortunately few systematic books on memory chip design have been written by experts This is a result of two factors the difficulty of involving university professors because of rapidly changing technology requiring huge investments and development resources and a shortage of time on the part of chip designers in industry due to severe competition in the memory chip business Therefore LSI memory chip design has been isolated from the outside preventing a deeper understanding of the technology This book is based on my 30 year memory chip particularly DRAM design career

High-speed Digital Design Howard W. Johnson, Martin Graham, 1993-01-01 Focused on the field of knowledge lying between digital and analog circuit theory this new text will help engineers working with digital systems shorten their product development cycles and help fix their latest design problems The scope of the material covered includes signal reflection crosstalk and noise problems which occur in high speed digital machines above 10 megahertz This volume will be of practical use to digital logic designers staff and senior communications scientists and all those interested in digital design

Nanometer SRAM and DRAM Circuit Design Paul S. Lazar, Yong-Bin Kim, Ban P. Wong, 2015-03-02 Low power consumption is moving higher up on the priority list of system requirements resulting in low power memory architectures and circuit implementations Circuits need to operate reliably at low voltages below 1.0V yet meet their performance targets The larger spread of process variations as the technology node shrinks makes worst case design impractical These design challenges impact also the peripheral control ancillary and I/O circuits of the SRAM and DRAM The goal of this book is to describe circuits and circuit design methodologies which overcome these challenges Examples of scripts which are used to steer the CAD tools used in the analysis of the circuits and to gather and present the results of such analysis are provided Since a variety of the emerging technologies such as CNT

Carbon Nano Tube and FinFET are being developed and researched a new design method for memory cell will also be discussed based on those emerging technologies

Fundamentals of High Frequency CMOS Analog Integrated Circuits
Duran Leblebici, Yusuf Leblebici, 2022-03-25 This textbook is ideal for senior undergraduate and graduate courses in RF CMOS circuits RF circuit design and high frequency analog circuit design It is aimed at electronics engineering students and IC design engineers in the field wishing to gain a deeper understanding of circuit fundamentals and to go beyond the widely used automated design procedures The authors employ a design centric approach in order to bridge the gap between fundamental analog electronic circuits textbooks and more advanced RF IC design texts The structure and operation of the building blocks of high frequency ICs are introduced in a systematic manner with an emphasis on transistor level operation the influence of device characteristics and parasitic effects and input output behavior in the time and frequency domains This second edition has been revised extensively to expand some of the key topics to clarify the explanations and to provide extensive design examples and problems New material has been added for basic coverage of core topics such as wide band LNAs noise feedback concept and noise cancellation inductive compensated band widening techniques for flat gain or flat delay characteristics and basic communication system concepts that exploit the convergence and co existence of Analog and Digital building blocks in RF systems A new chapter Chapter 5 has been added on Noise and Linearity addressing key topics in a comprehensive manner All of the other chapters have also been revised and largely re written with the addition of numerous solved design examples and exercise problems

CMOS Analog Integrated Circuits Tertulien Ndjountche, 2018-09-25 High speed power efficient analog integrated circuits can be used as standalone devices or to interface modern digital signal processors and micro controllers in various applications including multimedia communication instrumentation and control systems New architectures and low device geometry of complementary metaloxide semiconductor CMOS technologies have accelerated the movement toward system on a chip design which merges analog circuits with digital and radio frequency components

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