Signal and Power Integrity

SIMPLIFIED

Third Edition

Eric Bogatin

Modern Semiconductor Design Series
Signal Integrity Library

Eric Bogatin Signal Integrity

Christian G. Meyer

Eric Bogatin Signal Integrity:

Signal Integrity Eric Bogatin,2004 This thorough review of the fundamental principles associated with signal integrity provides engineering principles behind signal integrity effects and applies this understanding to solving problems **Signal and Power Integrity - Simplified** Eric Bogatin,2017-12-05 The 1 Practical Guide to Signal Integrity Design with Revised Content and New Questions and Problems This book brings together up to the minute techniques for finding fixing and avoiding signal integrity problems in your design Drawing on his work teaching several thousand engineers and graduate students world renowned expert Eric Bogatin systematically presents the root causes of all six families of signal integrity power integrity and electromagnetic compatibility problems Bogatin reviews essential principles needed to understand these problems and shows how to use best design practices and techniques to prevent or address them early in the design cycle To help test and reinforce your understanding this new edition adds questions and problems throughout Bogatin also presents more examples using free tools plus new content on high speed serial links reflecting input from 130 of his graduate students A fully up to date introduction to signal integrity and physical design New questions and problems designed for both students and professional engineers How design and technology selection can make or break power distribution network performance Exploration of key concepts such as plane impedance spreading inductance decoupling capacitors and capacitor loop inductance Practical techniques for analyzing resistance capacitance inductance and impedance Using QUCS to predict waveforms as voltage sources are affected by interconnect impedances Identifying reflections and crosstalk with free animation tools Solving signal integrity problems via rules of thumb analytic approximation numerical simulation and measurement Understanding how interconnect physical design impacts signal integrity Managing differential pairs and losses Harnessing the full power of S parameters in high speed serial link applications Designing high speed serial links associated with differential pairs and lossy lines including new coverage of eye diagrams Ensuring power integrity throughout the entire power distribution path Realistic design guidelines for improving signal integrity and much more For professionals and students at all levels of experience this book emphasizes intuitive understanding practical tools and engineering discipline rather than theoretical derivation or mathematical rigor It has earned a well deserved reputation as the 1 resource for getting signal integrity designs right first time every time *Bogatin's Practical Guide to Transmission Line Design and Characterization for Signal Integrity Applications* Eric Bogatin,2020-05-31 This multimedia eBook establishes a solid foundation in the essential principles of how signals interact with transmission lines how the physical design of interconnects affects transmission line properties and how to interpret single ended and differential time domain reflection TDR measurements to extract important figures of merits and avoid common mistakes This book presents an intuitive understanding of transmission lines Instructional videos are provided in every chapter that cover important aspects of the interconnect design and characterization process This video eBook helps establish foundations for designing and

characterizing the electrical properties of interconnects to explain in a simplified way how signals propagate and interact with interconnects and how the physical design of transmission structures will impact performance Never be intimidated by impedance or differential pairs again **Signal and Power Integrity - Simplified** Bogatin,2009 Signal Integrity Characterization Techniques Mike Resso,Eric Bogatin,2009 Cogently addressing the future of signal integrity and the effect it will have on the data transmission industry as a whole this all inclusive guide addresses a wide array of technologies from traditional digital data transmission to microwave measurements and accessibly examines the gap between the two Focusing on real world applications and providing a wide array of case studies that show how each technology can be used from backplane design challenges to advanced error correction techniques this guide addresses many of today s high speed technologies while also providing excellent insight into their future direction With numerous valuable lessons pertaining to the signal integrity industry this resource is the ultimate must read guide for any specialist in the design engineering field

Bogatin's Practical Guide to Prototype Breadboard and PCB Design Eric Bogatin,2021-09-30 Printed circuit boards PCB are at the heart of every electronic product manufactured today Yet engineers rarely learn to design PCBs from a class or course They learn it by doing by reading app notes watching YouTube videos and sitting by the side of an experienced engineer This book is the foundation building book for all engineers starting out to design PCBs It teaches good habits designing a PCB first for connectivity and secondly introduces the four most important principles to reduce noise A seven step process is presented developing a plan of record creating a Bill of Materials completing the schematic completing the layout completing the assembly conducting bring up and troubleshooting and documenting the project Each step is developed in detail In particular the emphasis in this book is on risk management what can be done at each step of the process to reduce the risk of a hard error which requires a complete re spin or a soft error which requires some sort of on the fly repair After connectivity is designed it s important to develop good habits to minimize the potential noise from ground bounce power rail stitching noise stack up design and reducing switching noise in signal paths These techniques apply to all designs from 2 layer to 8 layer and more for bandwidths below 200 MHz The best practices for manual lead free soldering are presented so that everyone can become a soldering expert The best measurement practices using common lab instruments such as the DMM the constant current constant voltage power supply and oscilloscopes are presented so that common artifacts are minimized Features in the design that help you find design or assembly errors quickly and the troubleshooting techniques to find and fix problems are introduced Applying the habits presented in this book will help every engineer design their next circuit board faster with less chance of an unexpected problem with the lowest noise This textbook will also have embedded videos to visually demonstrate many of the hands on processes introduced in this book **Signal and Power Integrity--simplified** Eric Bogatin,2010 With the inclusion of the two new hot topics in signal integrity power integrity and high speed serial links this book will be the most up to date complete guide to understanding and designing for

signal integrity **Signal and Power Integrity-simplified (Third Edition)** Eric Bogatin,2021 **A Signal Integrity Engineer's Companion** Geoff Lawday,David Ireland,Greg Edlund,2008-06-12 A Signal Integrity Engineer s Companion Real Time Test and Measurement and Design Simulation Geoff Lawday David Ireland Greg Edlund Foreword by Chris Edwards Editor IET Electronics Systems and Software magazine Prentice Hall Modern Semiconductor Design Series Prentice Hall Signal Integrity Library Use Real World Test and Measurement Techniques to Systematically Eliminate Signal Integrity Problems This is the industry s most comprehensive authoritative and practical guide to modern Signal Integrity SI test and measurement for high speed digital designs Three of the field s leading experts guide you through systematically detecting observing analyzing and rectifying both modern logic signal defects and embedded system malfunctions The authors cover the entire life cycle of embedded system design from specification and simulation onward illuminating key techniques and concepts with easy to understand illustrations Writing for all electrical engineers signal integrity engineers and chip designers the authors show how to use real time test and measurement to address today s increasingly difficult interoperability and compliance requirements They also present detailed start to finish case studies that walk you through commonly encountered design challenges including ensuring that interfaces consistently operate with positive timing margins without incurring excessive cost calculating total jitter budgets and managing complex tradeoffs in high speed serial interface design Coverage includes Understanding the complex signal integrity issues that arise in today s high speed designs Learning how eye diagrams automated compliance tests and signal analysis measurements can help you identify and solve SI problems Reviewing the electrical characteristics of today s most widely used CMOS IO circuits Performing signal path analyses based on intuitive Time Domain Reflectometry TDR techniques Achieving more accurate real time signal measurements and avoiding probe problems and artifacts Utilizing digital oscilloscopes and logic analyzers to make accurate measurements in high frequency environments Simulating real world signals that stress digital circuits and expose SI faults Accurately measuring jitter and other RF parameters in wireless applications About the Authors Dr Geoff Lawday is Tektronix Professor in Measurement at Buckinghamshire New University England He delivers courses in signal integrity engineering and high performance bus systems at the University Tektronix laboratory and presents signal integrity seminars throughout Europe on behalf of Tektronix David Ireland European and Asian design and manufacturing marketing manager for Tektronix has more than 30 years of experience in test and measurement He writes regularly on signal integrity for leading technical journals Greg Edlund Senior Engineer IBM Global Engineering Solutions division has participated in development and testing for ten high performance computing platforms He authored Timing Analysis and Simulation for Signal Integrity Engineers Prentice Hall **Signal Integrity - Simplified** ,2009 **Timing Analysis and Simulation for Signal Integrity Engineers** Greg Edlund,2007-10-22 Every day companies call upon their signal integrity engineers to make difficult decisions about design constraints and timing margins Can I move these wires closer together How many holes

can I drill in this net How far apart can I place these chips Each design is unique there s no single recipe that answers all the questions Today s designs require ever greater precision but design guides for specific digital interfaces are by nature conservative Now for the first time there s a complete guide to timing analysis and simulation that will help you manage the tradeoffs between signal integrity performance and cost Writing from the perspective of a practicing SI engineer and team lead Greg Edlund of IBM presents deep knowledge and quantitative techniques for making better decisions about digital interface design Edlund shares his insights into how and why digital interfaces fail revealing how fundamental sources of pathological effects can combine to create fault conditions You won t just learn Edlund s expert techniques for avoiding failures you ll learn how to develop the right approach for your own projects and environment Coverage includes Systematically ensure that interfaces will operate with positive timing margin over the product s lifetime without incurring excess cost Understand essential chip to chip timing concepts in the context of signal integrity Collect the right information upfront so you can analyze new designs more effectively Review the circuits that store information in CMOS state machines and how they fail Learn how to time common clock source synchronous and high speed serial transfers Thoroughly understand how interconnect electrical characteristics affect timing propagation delay impedance profile crosstalk resonances and frequency dependent loss Model 3D discontinuities using electromagnetic field solvers Walk through four case studies coupled differential vias land grid array connector DDR2 memory data transfer and PCI Express channel Appendices present a refresher on SPICE modeling and a high level conceptual framework for electromagnetic field behavior Objective realistic and practical this is the signal integrity resource engineers have been searching for Preface xiii Acknowledgments xvi About the Author xix About the Cover xx Chapter 1 Engineering Reliable Digital Interfaces 1 Chapter 2 Chip to Chip Timing 13 Chapter 3 Inside IO Circuits 39 Chapter 4 Modeling 3D Discontinuities 73 Chapter 5 Practical 3D Examples 101 Chapter 6 DDR2 Case Study 133 Chapter 7 PCI Express Case Study 175 Appendix A A Short CMOS and SPICE Primer 209 Appendix B A Stroll Through 3D Fields 219 Endnotes 233 Index 235 Jitter, Noise, and Signal Integrity at High-Speed Mike Peng Li,2007-11-19 State of the art JNB and SI Problem Solving Theory Analysis Methods and Applications Jitter noise and bit error JNB and signal integrity SI have become today s greatest challenges in high speed digital design Now there s a comprehensive and up to date guide to overcoming these challenges direct from Dr Mike Peng Li cochair of the PCI Express jitter standard committee One of the field s most respected experts Li has brought together the latest theory analysis methods and practical applications demonstrating how to solve difficult JNB and SI problems in both link components and complete systems Li introduces the fundamental terminology definitions and concepts associated with JNB and SI as well as their sources and root causes He guides readers from basic math statistics circuit and system models all the way through final applications Emphasizing clock and serial data communications applications he covers JNB and SI simulation modeling diagnostics debugging compliance testing and much more **Design for Electromagnetic**

Compatibility--In a Nutshell Reto B. Keller,2022-11-22 This open access book provides practicing electrical engineers and students a practical and mathematically sound introduction to the topic of electromagnetic compatibility EMC The author enables readers to understand better how to overcome commonly failed EMC tests for radiated emission radiated immunity and electrostatic discharge ESD while providing concrete EMC design guidelines The book also presents an overview of EMC standards and regulations and how to test for a global market access Complete PCB Design Using OrCAD Capture and PCB Editor Kraig Mitzner,2009-05-28 This book provides instruction on how to use the OrCAD design suite to design and manufacture printed circuit boards The primary goal is to show the reader how to design a PCB using OrCAD Capture and OrCAD Editor Capture is used to build the schematic diagram of the circuit and Editor is used to design the circuit board so that it can be manufactured The book is written for both students and practicing engineers who need in depth instruction on how to use the software and who need background knowledge of the PCB design process Beginning to end coverage of the printed circuit board design process Information is presented in the exact order a circuit and PCB are designed Over 400 full color illustrations including extensive use of screen shots from the software allow readers to learn features of the product in the most realistic manner possible Straightforward realistic examples present the how and why the designs work providing a comprehensive toolset for understanding the OrCAD software Introduces and follows IEEE IPC and JEDEC industry standards for PCB design Unique chapter on Design for Manufacture covers padstack and footprint design and component placement for the design of manufacturable PCB s FREE CD containing the OrCAD demo version and design files

Complete PCB Design Using OrCad Capture and Layout Kraig Mitzner,2011-04-01 Complete PCB Design Using OrCad Capture and Layout provides instruction on how to use the OrCAD design suite to design and manufacture printed circuit boards The book is written for both students and practicing engineers who need a quick tutorial on how to use the software and who need in depth knowledge of the capabilities and limitations of the software package There are two goals the book aims to reach The primary goal is to show the reader how to design a PCB using OrCAD Capture and OrCAD Layout Capture is used to build the schematic diagram of the circuit and Layout is used to design the circuit board so that it can be manufactured The secondary goal is to show the reader how to add PSpice simulation capabilities to the design and how to develop custom schematic parts footprints and PSpice models Often times separate designs are produced for documentation simulation and board fabrication This book shows how to perform all three functions from the same schematic design This approach saves time and money and ensures continuity between the design and the manufactured product Information is presented in the exact order a circuit and PCB are designed Straightforward realistic examples present the how and why the designs work providing a comprehensive toolset for understanding the OrCAD software Introduction to the IPC JEDEC and IEEE standards relating to PCB design Full color interior and extensive illustrations allow readers to learn features of the product in the most realistic manner possible **Real World FPGA Design with Verilog** Ken Coffman,1999-12-08 The

practical guide for every circuit designer creating FPGA designs with Verilog Walk through design step by step from coding through silicon Partitioning synthesis simulation test benches combinatorial and sequential designs and more Real World FPGA Design with Verilog guides you through every key challenge associated with designing FPGAs and ASICs using Verilog one of the world s leading hardware design languages You ll find irreverent yet rigorous coverage of what it really takes to translate HDL code into hardware and how to avoid the pitfalls that can occur along the way Ken Coffman presents no frills real world design techniques that can improve the stability and reliability of virtually any design Start by walking a typical Verilog design all the way through to silicon then review basic Verilog syntax design simulation and testing advanced simulation and more Coverage includes Essential digital design strategies recognizing the underlying analog building blocks used to create digital primitives implementing logic with LUTs clocking strategies logic minimization and more Key engineering tradeoffs including operating speed vs latency Combinatorial and sequential designs Verilog test fixtures compiler directives and automated testing A detailed comparison of alternative architectures and software including a never before published FPGA technology selection checklist Real World FPGA Design with Verilog introduces libraries and reusable modules points out opportunities to reuse your own code and helps you decide when to purchase existing IP designs instead of building from scratch Essential rules for designing with ASIC conversion in mind are presented If you re involved with digital hardware design with Verilog Ken Coffman is a welcome voice of experience showing you the shortcuts helping you over the rough spots and helping you achieve competence faster than you ever expected **Advances in Electronic Testing** Dimitris Gizopoulos,2006-01-22 This is a new type of edited volume in the Frontiers in Electronic Testing book series devoted to recent advances in electronic circuits testing The book is a comprehensive elaboration on important topics which capture major research and development efforts today Hot topics of current interest to test technology community have been selected and the authors are key contributors in the corresponding topics Signal Integrity Issues and Printed Circuit Board Design Douglas Brooks,2003 Complicated concepts explained succinctly and in laymen s terms to both experienced and novice PCB designers Numerous examples allow reader to visualize how high end software simulators see various types of SI problems and then their solutions Author is a frequent and recognized seminar leader in the industry *Measurement Technology and its Application III* Prasad Yarlagadda,Yun Hae Kim,2014-06-10 Selected peer reviewed papers from the 2014 International Conference on Measurement Instrumentation and Automation ICMIA 2014 April 23 24 2014 Shanghai China

Digital Communications Test and Measurement Dennis Derickson,Marcus Müller,2007-12-10 A Comprehensive Guide to Physical Layer Test and Measurement of Digital Communication Links Today s new data communication and computer interconnection systems run at unprecedented speeds presenting new challenges not only in the design but also in troubleshooting test and measurement This book assembles contributions from practitioners at top test and measurement companies component manufacturers and universities It brings together information that has never been broadly accessible

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Eric Bogatin Signal Integrity Introduction

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