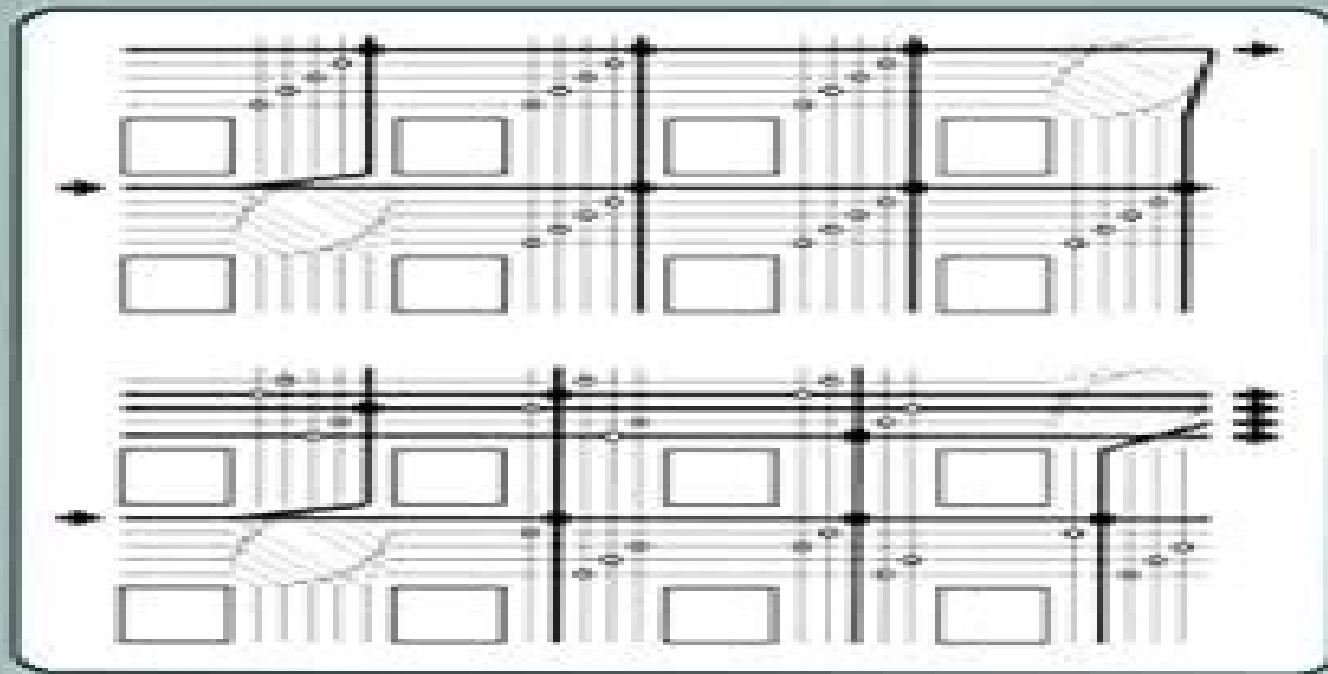


Design of Interconnection Networks for Programmable Logic

Guy Lemieux
David Lewis



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