DIRECT DIGITAL SYNTHESIZERS

Theory, Design and Applications

Jouko Vankka and Kari Halonen

<u>Direct Digital Synthesizers Theory Design And</u> <u>Applications Author Jouko Vankka Jul 2001</u>

Nasserullah Khan, University of Waterloo. Department of Electrical and Computer Engineering

Direct Digital Synthesizers Jouko Vankka, Kari A.I. Halonen, 2013-04-17 A major advantage of a direct digital synthesizer DDS is that its output frequency phase and amplitude can be precisely and rapidly manipulated under digital processor control Other inherent DDS attributes include the ability to tune with extremely fine frequency and phase resolution and to rapidly hop between frequencies These combined characteristics have made the technology popular in military radar and communications systems In fact DDS technology was previously applied almost exclusively to high end and military applications it was costly power hungry difficult to implement and required a discrete high speed D A converter Due to improved integrated circuit IC technologies they now present a viable alternative to analog based phase locked loop PLL technology for generating agile analog output frequency in consumer synthesizer applications It is easy to include different modulation capabilities in the DDS by using digital signal processing DSP methods because the signal is in digital form By programming the DDS adaptive channel bandwidths modulation formats frequency hopping and data rates are easily achieved The flexibility of the DDS makes it ideal for signal generator for software radio The digital circuits used to implement signal processing functions do not suffer the effects of thermal drift aging and component variations associated with their analog counterparts The implementation of digital functional blocks makes it possible to achieve a high degree of system integration Recent advances in IC fabrication technology particularly CMOS coupled with advanced DSP algorithms and architectures are providing possible single chip DDS solutions to complex communication and signal processing subsystems as modulators demodulators local oscillators programmable clock generators and chirp generators The DDS addresses a variety of applications including cable modems measurement equipments arbitrary waveform generators cellular base stations and wireless local loop base stations Direct Digital Synthesizers was written to find possible applications for radio communication systems It will have appeal for wireless and wireline communication engineers teachers and students

Digital Synthesizers and Transmitters for Software Radio Jouko Vankka, 2005-10-25 The approach adopted in Digital Synthesizers and Transmitters for Software Radio will provide an understanding of key areas in the field of digital synthesizers and transmitters It is easy to include different digital techniques in the digital synthesizers and transmitters by using digital signal processing methods because the signal is in digital form By programming the digital synthesizers and transmitters adaptive channel bandwidths modulation formats frequency hopping and data rates are easily achieved Techniques such as digital predistortion for power amplifier linearization digital compensation methods for analog I Q modulator nonlinearities and digital power control and ramping are presented in this book The flexibility of the digital synthesizers and transmitters makes them ideal as signal generators for software radio Software radios represent a major change in the design paradigm for radios in which a large portion of the functionality is implemented through programmable signal processing devices giving the radio the ability to change its operating parameters to accommodate new features and

capabilities A software radio approach reduces the content of radio frequency RF and other analog components of traditional radios and emphasizes digital signal processing to enhance overall transmitter flexibility Software radios are emerging in commercial and military infrastructure **Direct Digital Frequency Synthesizers** Venceslav F. Kroupa, 1998-11-18 With the advent of integrated circuits IC digital systems have become widely used in modern electronic devices including communications and measurement equipment Direct Digital Frequency Synthesizers DDS are used in communications as transmitter exciters and local oscillators in receivers. The advantages are superior frequency stability the same as that of the driving clockoscillator and short switching times The difficulties are loweroutput frequencies and rather large spurious signals Compiled for practicing engineers who do not have the prerequisite of a specialist s knowledge in Direct DigitalFrequency Synthesizers DDS this collection of 40 important printed papers and 9 never before published contributions presents a comprehensive introduction to DDS properties and a clearunderstanding of actual devices The information in this volume canlead to easier computer simulations and improved designs Featured topics include Discussion of principles and state of the art of wide rangeDDS Investigation of spurious signals in DDS Combination of DDS with Phase Lock Loops PLL Examination of phase and background noise in DDS Introduction to Digital to Analog Conversion DAC Analysis of mathematics of quasiperiodic omission ofpulses DDFS can also serve as a textbook for students seeking A Modulator Using Direct Digital Synthesis Technique Jouko Vankka,1996 essentialbackground theory

Microwave and Wireless Synthesizers Ulrich L. Rohde, Enrico Rubiola, Jerry C. Whitaker, 2021-03-29 The new edition of the leading resource on designing digital frequency synthesizers from microwave and wireless applications fully updated to reflect the most modern integrated circuits and semiconductors Microwave and Wireless Synthesizers Theory and Design Second Edition remains the standard text on the subject by providing complete and up to date coverage of both practical and theoretical aspects of modern frequency synthesizers and their components Featuring contributions from leading experts in the field this classic volume describes loop fundamentals noise and spurious responses special loops loop components multiloop synthesizers and more Practical synthesizer examples illustrate the design of a high performance hybrid synthesizer and performance measurement techniques offering readers clear instruction on the various design steps and design rules The second edition includes extensively revised content throughout including a modern approach to dealing with the noise and spurious response of loops and updated material on digital signal processing and architectures Reflecting today s technology new practical and validated examples cover a combination of analog and digital synthesizers and hybrid systems Enhanced and expanded chapters discuss implementations of direct digital synthesis DDS architectures the voltage controlled oscillator VCO crystal and other high Q based oscillators arbitrary waveform generation vector signal generation and other current tools and techniques Now requiring no additional literature to be useful this comprehensive one stop resource Provides a fully reviewed updated and enhanced presentation of microwave and wireless synthesizers Presents a

clear mathematical method for designing oscillators for best noise performance at both RF and microwave frequencies Contains new illustrations figures diagrams and examples Includes extensive appendices to aid in calculating phase noise in free running oscillators designing VHF and UHF oscillators with CAD software using state of the art synthesizer chips and generating millimeter wave frequencies using the delay line principle Containing numerous designs of proven circuits and more than 500 relevant citations from scientific journal and papers Microwave and Wireless Synthesizers Theory and Design Second Edition is a must have reference for engineers working in the field of radio communication and the perfect textbook for advanced electrical engineering students Frequency Synthesizers Vadim Manassewitsch, 1987-03-30 Praise for the first edition This is a very readable full and stimulating account of the theory and practice of frequency synthesizers which contains much important material not previously gathered in one volume. The text is illustrated by many clear examples taken from practical situations often involving manufacturers devices and specifications. The book is marked by a clear lucid style and the illustration of particular points by practical examples is first class The diagrams are most helpful and the notation is clear Certainly design engineers would find this book extremely useful as a reference text Students and nonexperts will find the material understandable and readable P Daly University of Leeds England in Circuit Theory and Applications Frequency Synthesizers Theory and Design is the third edition of Vadim Manassewitsch's definitive treatment of the subject Updated to include the latest achievements in the performance of crystal controlled oscillators the design theory of fast switching time synthesizers and an example of their practical applications the book continues to be a complete guide for everyone who works with synthesizers Intended to formulate basic design principles and to demonstrate design procedures meeting several stringent requirements simultaneously its emphasis is on high speed synthesis and its new applications in radar spread spectrum communications automatic test equipment and nuclear magnetic resources Manassewitsch describes numerous approaches to ultra stable signal sources generating spectrally pure signals of high accuracy and shows how various building blocks such as mixers oscillators and frequency multipliers and dividers are used in frequency synthesis To meet the needs of engineers in this rapidly growing field Manassewitsch has added several novel frequency synthesis techniques developed the principles of high speed synthesis and described new synthesizers using important design approaches A summary of the most recent developments in frequency generation and control the book is firmly based on the realities of current design practices in the United States as well as abroad With an intermodulation products chart among its figures a computer program which calculates the frequencies of mixer intermodulation products among its appendices and a bibliography of more than 190 references Frequency Synthesizers Theory and Design will continue to be an invaluable aid for engineers managers instructors and students Microwave and Wireless Synthesizers Ulrich L. Rohde, 1997-08-25 Over the past decade great strides have been made in the technology of microwave oscillators and synthesizers with digital frequency synthesizers in particular attracting much attention These synthesizers are now being used in virtually all modern signal generators and

radio communication equipment Until now however detailed information about their design has been hard to come by much of it scattered through journal articles and most books on the subject have taken a primarily theoretical approach Enter Microwave and Wireless Synthesizers the first book to emphasize both practical circuit information from RF to millimeter wave frequencies and up to date theory Based on course material taught by author Ulrich L Rohde at George Washington University and recent work done by the author at Compact Software Inc and Synergy Microwave Corporation this volume is a complete revision and update of Rohde's landmark text Digital PLL Frequency Synthesizers Theory and Design While it provides all the necessary theory and formulas it also offers an in depth look at the practical side of the phase lock loop PLL in synthesizers including special loops loop components and practical circuits material that is not available in any other book Rohde explains loop fundamentals demonstrates the linear approach to oscillator phase noise discusses the digital direct synthesizer technique addresses low noise oscillator design and provides insight into the role and design of crystal oscillators mixers phase frequency discriminators wideband high gain amplifiers programmable dividers and loop filters He goes on to cover conventional multiloop synthesizers and survey existing state of the art microwave synthesizer applications Extensive appendices review the mathematics of useful functions and various applications including even the complex nonlinear theory of noise in large signal systems such as mixers and oscillators Microwave and Wireless Synthesizers allows anyone with a PC running either Windows 3 11 or Windows NT to explore real world design It uses programs for the solution of digital phase lock loop systems tabulates the results and shows how Bode diagrams are determined by the computer's graphic capabilities It also includes examples using commercially available linear and nonlinear CAD programs to provide accurate evaluation and optimization of oscillators and other useful circuits and many practical charts For companies involved in test and communication equipment this book reduces design and research costs by providing a large number of proven circuits and expediting the design process It is also an outstanding senior graduate level textbook for electrical engineering students and an invaluable resource for practicing engineers senior engineers and managers who would like to be able to evaluate new trends and techniques in the field Design of Direct Digital Frequency Synthesizer for Wireless Applications Lakshmi Sri Ivothi Chimakurthy, 2005 High-Speed and High-Performance Direct Digital Frequency Synthesizer Design Jun-an Zhang, Ruitao Zhang, Guangjun Li, 2022-03-23 The book focuses on design technology of high speed and high performance direct digital frequency synthesizer DDS chip The technologies involves phase to amplitude converter design D A converter design phase accumulator design multi chip synchronization circuit design etc In each chapter the concept of the technology is explained first and then the features of different implementation schemes are introduced through the real design cases More over a design case of a 2 5GHz monolithic DDS in 0 18 m CMOS which was designed by the authors are introduced in detail which can help the reader understanding about the of DDS design deeply The book is suitable for the readers who are interested to learn practical design technology in DDS The book can benefit researchers engineers and graduate students in

Synthesizer Fundamentals with Emphasis on Direct Digital Synthesizers Gary D. Parsons,1992 Design of Pulse Output Direct Digital Synthesizer with an Analog Filter Bank Aditya Pothuri,2008 Over the past two decades frequency synthesizer has progressed from a relatively crystal bank synthesizer to multi loop phase locked loops But in recent years direct digital synthesizers DDS have witnessed an increase in demand over their analog counterparts The DDS offers a wide range of advantages over phase locked loops such as high frequency range fast switching response and sub hertz frequency resolution However the conventional DDS suffers limitations due to its architecture involving digital to analog converter DAC and read only memory ROM with respect to high clock speeds The Pulse Output Direct Digital Synthesizer PODDS presented in this thesis eliminates the necessity for a DAC and ROM and replaces the phase conversion block present in the conventional DDS with an Analog filter bank to generate the sine wave The PODDS operates at a frequency of 2 25GHz with a 16 b phase accumulator designed using carry look ahead adder The analog filter bank was designed using an array of low pass Chebyshev filters A frequency range of 1 1 MHz to 1 125GHz was achieved with average power consumption of 83mW Direct Digital Frequency Synthesizer for Mobile Wireless Applications Nasserullah Khan, University of Waterloo.

Department of Electrical and Computer Engineering, 2001 **Direct digital synthesizers** Thomas Edward Brand, 1990

Frequency Synthesizers Manassewitsch, 1997-02-01 Direct Digital Synthesizers, 1985 Frequency synthesizers are one of the principal building blocks of precise time and frequency system Direct digital synthesizers DDS have become increasingly important as the need for small low power high resolution wide frequency range fast settling high spectral purity synthesizers has grown This paper reviews the various types of DDS s and the principles of their design DDS architectures fall into 6 major categories pulse output sine output triangle output phase interpolation jitter injection and fractional divider or pulse snatching DDS s The details design principles and performance characteristics of these categories are reviewed and the pros and cons of one category verses another are presented included is a discussion of the relationships between the design parameters of the various types of DDS s and synthesizer performance parameters such as spectral purity phase jitter frequency range frequency resolution and settling time Of prime importance in the design of these DDS s are the requirements on spurious sideband and phase jitter levels A theory for predicting these spur and jitter levels and of relating these levels to DDS design parameters is presented It is shown that DDS spurs can be understood as coming from harmonic distortion in the DDS output which is aliased down to lower frequencies due to the discrete stepped nature of DDS operation The relationship of the sizes and frequencies of these aliasing spurs to DDS parameters is discussed showing that spur considerations place fundamental limitations on the permissible frequency range of the DDS A quantitative theory explaining how jitter injection reduces spur levels is also presented Design of a Direct Digital Frequency Synthesizer for <u>Digital Down Conversion</u> Roan Marlon Nicholson, 2004 Analysis of the Direct Digital Synthesizer (DDS) Spectrum

Mijung Roperti,2009 Design of a Direct Digital Frequency Synthesizer Christopher Robert Cole,1982

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