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VLSI-SoC: Technology Advancement on SoC Design

29th IFIP WG 10.5/IEEE International Conference
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Keutzer,2005-06-28 An increasing number of system designers are using ASIPs rather than ASICs to implement their system solutions Building ASIPs The Mescal Methodology gives a simple but comprehensive methodology for the design of these application specific instruction processors ASIPs The key elements of this methodology are Judiciously using benchmarking Inclusively identifying the architectural space Efficiently describing and evaluating the ASIPs Comprehensively exploring the design space Successfully deploying the ASIP This book includes demonstrations of applications of the methodologies using the Tipi research framework as well as state of the art commercial toolsets from CoWare and Tensilica Recent Advances in Intrusion Detection Somesh Jha,Robin Sommer,Christian Kreibich,2010-09-02

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protection high performance malware detection and defence evaluation forensics anomaly detection as well as web security

VLSI-SoC: The Advanced Research for Systems on Chip Salvador Mir, Chi-Ying Tsui, Ricardo Reis, Oliver C.S.

Choy, 2012-09-25 This book contains extended and revised versions of the best papers presented at the 19th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2011 held in Hong Kong China in October 2011. The 10 papers included in the book were carefully reviewed and selected from the 45 full papers and 16 special session papers presented at the conference. The papers cover a wide range of topics in VLSI technology and advanced research. They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems.

VLSI-SoC: Technologies for Systems Integration Jürgen Becker, Marcelo De Oliveira Johann, Ricardo Reis, 2013-11-27

This book contains extended and revised versions of the best papers presented at the 17th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2009 held in Florianópolis, Brazil in October 2009. The 8 papers included in the book together with two keynote talks were carefully reviewed and selected from 27 papers presented at the conference. The papers cover a wide variety of excellence in VLSI technology and advanced research addressing the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems.

VLSI-SoC: New Technology

Enabler Carolina Metzler, Pierre-Emmanuel Gaillardon, Giovanni De Micheli, Carlos Silva-Cardenas, Ricardo Reis, 2020-07-22

This book contains extended and revised versions of the best papers presented at the 27th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2019 held in Cusco, Peru in October 2019. The 15 full papers included in this volume were carefully reviewed and selected from the 28 papers out of 82 submissions presented at the conference. The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies. In particular, they address cutting edge research fields like heterogeneous neuromorphic and brain inspired biologically inspired approximate computing systems.

VLSI-SoC: Design Trends Andrea Calimera, Pierre-Emmanuel Gaillardon, Kunal

Korgaonkar, Shahar Kvatinsky, Ricardo Reis, 2021-07-14 This book contains extended and revised versions of the best papers presented at the 28th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2020 held in Salt Lake City, UT, USA in October 2020. The 16 full papers included in this volume were carefully reviewed and selected from the 38 papers out of 74 submissions presented at the conference. The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies. In particular, they address cutting edge research fields like low power design of RF analog and mixed signal circuits, EDA tools for the synthesis and verification of heterogeneous

SoCs accelerators for cryptography and deep learning and on chip Interconnection system reliability and testing and integration of 3D ICs The conference was held virtually **VLSI-SoC: Design for Reliability, Security, and Low Power** Youngsoo Shin, Chi Ying Tsui, Jae-Joon Kim, Kiyoun Choi, Ricardo Reis, 2016-09-12 This book contains extended and revised versions of the best papers presented at the 23rd IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2015 held in Daejeon Korea in October 2015 The 10 papers included in the book were carefully reviewed and selected from the 44 full papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about new challenges both at the physical and system design levels as well as in the test of these systems **VLSI-SoC: Design Methodologies for SoC and SiP** Christian Pigué, Ricardo Reis, Dimitrios Soudris, 2010-04-06 This book contains extended and revised versions of the best papers that were presented during the 16th edition of the IFIP IEEE WG10.5 International Conference on Very Large Scale Integration a global System on a Chip Design CAD conference The 16th conference was held at the Grand Hotel of Rhodes Island Greece October 13-15 2008 Previous conferences have taken place in Edinburgh Trondheim Vancouver Munich Grenoble Tokyo Gramado Lisbon Montpellier Darmstadt Perth Nice and Atlanta VLSI SoC 2008 was the 16th in a series of international conferences sponsored by IFIP WG 10.5 and IEEE CEDA that explores the state of the art and the new developments in the field of VLSI systems and their designs The purpose of the conference was to provide a forum to exchange ideas and to present industrial and research results in the fields of VLSI ULSI systems embedded systems and microelectronic design and test **VLSI-SoC: At the Crossroads of Emerging Trends** Alex Orailoglu, H. Fatih Ugurdag, Luís Miguel Silveira, Martin Margala, Ricardo Reis, 2016-08-23 This book contains extended and revised versions of the best papers presented at the 21st IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2013 held in Istanbul Turkey in October 2013 The 11 papers included in the book were carefully reviewed and selected from the 48 full papers presented at the conference An extended version of a previously unpublished high quality paper from VLSI SoC 2012 is also included The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems **VLSI-SoC: Forward-Looking Trends in IC and Systems Design** Jose L. Ayala, David Atienza Alonso, Ricardo Reis, 2014-04-13 This book contains extended and revised versions of the best papers presented at the 18th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2010 held in Madrid Spain in September 2010 The 14 papers included in the book were carefully reviewed and selected from the 52 full papers presented at the conference The papers cover a wide variety of excellence in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing

about stimulating new challenges both at the physical and system design levels as well as in the test of these systems

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