

FLASHWAVE® 4500

Multiservice Provisioning Platform

Features and specifications

Architectures	<ul style="list-style-type: none"> Terminal Linear ADM (1+1) Unidirectional Path Switched Ring (UPSR) Two Fiber-Bidirectional Line Switched Ring (2F-BLSR) Four Fiber-Bidirectional Line Switched Ring (4F-BLSR) Dedicated Path Protected Mesh (DPPM) Resilient Packet Ring (RPR) 	
Interfaces	DS1	64-pin AMP® OHAMP connectors
	DS3/EC1	BNC connectors
	OC-3/STM-1	SC for dual unit; LC for quad unit; MPO connector for 8-port unit 1310 nm wideband
	OC-12/STM-4	LC, FC, SC or ST for single unit; LC for quad unit; MPO connector for 8-port unit 1310 nm wideband 1550 nm wideband (single unit only)
	OC-48/STM-16	LC, FC, SC or ST connectors 1310 nm wideband 1550 nm tunable narrowband (8 λ @ 50 GHz)
	OC-192/STM-64	LC, FC, SC or ST connectors 1310 nm wideband; 1550 nm wideband 1550 nm tunable narrowband (4 λ @ 100 GHz) 1550 nm full band tunable to 88 λ, (@ 50-GHz spacing) G.709 compliant
	Ethernet (EoS)	RJ-45 connectors for 10/100Base-T Ethernet; LC or SC connectors for Gigabit Ethernet
	Ethernet Aggregation (trunking port, RPR)	LC connectors for dual port unit 850 nm wideband; 1310 nm wideband High/Low Order Aggregation

Switching

Maximum Switching Capacity				
Specification	Single MSPP	Main Shelf MSPP/MSPP	Double Shelf MSPP	Triple Shelf MSPP
Switch Capacity	70 Gbps	140 Gbps	220 Gbps	300 Gbps
STS-1 Cross-Connects	1344 x 1344	2688 x 2688* 5760 x 5760**	5760 x 5760	5760 x 5760
VTT-5 Cross-Connects	5376 x 5376 (10 Gbps)	21,504 x 21,504 (40 Gbps)	21,504 x 21,504 (40 Gbps)	21,504 x 21,504 (40 Gbps)

* Requires 140 Gbps switch fabric

** Requires 300 Gbps switch fabric

Protection	DS1	1+1 or unprotected
	DS3/EC1	1:n (n=1 to 6) or unprotected
	DS3 Transmux	1+1 or 1:n (n=1 to 6)
	OC-3/STM-1	1+1, UPSR, DPPM or unprotected
	OC-12/STM-4	1+1, UPSR, DPPM or unprotected
	OC-48/STM-16	1+1, UPSR, 2F-BLSR, DPPM or unprotected
	OC-192/STM-64	1+1, UPSR, 2F-BLSR, 4F-BLSR, DPPM or unprotected
	Ethernet	RPR or unprotected Port Link Aggregation or Unit Link Aggregation
	Switch matrices	1+1
	Synchronization	1+1
	Synchronization	<ul style="list-style-type: none"> Internal Stratum 3 timing source Synchronization Status Messaging (SSM) DS1 Building Integrated Timing Supply (BITS) primary and secondary clock output/input Line timing

Maximum Number of Unprotected (Protected) Service Interfaces

Type	Ports/ Card	Single Shelf MSPP/MSPP	Double Shelf MSPP	Triple Shelf MSPP
DS1 (Integrated)	28	84 (84)	168 (168)	252 (252)
DS1 (High-Density)	168	6720	13,440	20,160
DS3/EC1	8	96 (96)	192 (192)	288 (288)
DS3 Transmux	12	144	288	432
OC-3/3c/STM-1	8	160 (80)	320 (160)	480 (240)
OC-12/12c/STM-4	8	160 (80)	320 (160)	480 (240)
OC-48/48c/STM-16	2	40 (20)	80 (40)	120 (60)
OC-192/192c/STM-64	1	10 (5)	20 (10)	30 (15)
10/100Base-T Ethernet	4 (8)	80	160	240
Gigabit Ethernet	2	40	80	120

Flashwave Config Guide

**State Library of Louisiana, Broadband
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Table of Contents Flashwave Config Guide

1. Understanding the eBook Flashwave Config Guide
 - The Rise of Digital Reading Flashwave Config Guide
 - Advantages of eBooks Over Traditional Books
2. Identifying Flashwave Config Guide
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Flashwave Config Guide
 - User-Friendly Interface
4. Exploring eBook Recommendations from Flashwave Config Guide
 - Personalized Recommendations
 - Flashwave Config Guide User Reviews and Ratings
 - Flashwave Config Guide and Bestseller Lists
5. Accessing Flashwave Config Guide Free and Paid eBooks
 - Flashwave Config Guide Public Domain eBooks
 - Flashwave Config Guide eBook Subscription Services
 - Flashwave Config Guide Budget-Friendly Options
6. Navigating Flashwave Config Guide eBook Formats
 - ePub, PDF, MOBI, and More
 - Flashwave Config Guide Compatibility with Devices
 - Flashwave Config Guide Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Flashwave Config Guide
 - Highlighting and Note-Taking Flashwave Config Guide
 - Interactive Elements Flashwave Config Guide
8. Staying Engaged with Flashwave Config Guide

- Joining Online Reading Communities
- Participating in Virtual Book Clubs
- Following Authors and Publishers Flashwave Config Guide
- 9. Balancing eBooks and Physical Books Flashwave Config Guide
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Flashwave Config Guide
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Flashwave Config Guide
 - Setting Reading Goals Flashwave Config Guide
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Flashwave Config Guide
 - Fact-Checking eBook Content of Flashwave Config Guide
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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