

Expt. No.1

**APPLICATIONS OF OP-AMP - I
(INVERTING AND NON - INVERTING AMPLIFIER)**

I. a. INVERTING AMPLIFIER

AIM:

To design an Inverting Amplifier for the given specifications using Op-Amp IC 741.

APPARATUS REQUIRED:

S.No	Name of the Apparatus	Range	Quantity
1.	Function Generator	3 MHz	1
2.	CRO	30 MHz	1
3.	Dual RPS	0 - 30 V	1
4.	Op-Amp	IC 741	1
5.	Bread Board		1
6.	Resistors		10
7.	Capacitors		10
8.	Jumper wires		10

Digital Logic Circuits Lab Manual

Lei Huang

Digital Logic Circuits Lab Manual:

Digital Circuit Design Laboratory Manual, 4th edition (Global) Akhan Almagambetov, J. Matt Pavlina, Yelena Mukhortova,

Digital Electronics and Microprocessors Lab Manual Mr. Rohit Manglik, 2024-04-06 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels *Lab Manual* Gregory L. Moss, 1991

Introduction to Digital Logic Textbook Equity, 2014-09-10 This textbook released under a Creative Commons Share Alike CC BY SA license is presented in its original format with the academic content unchanged It was authored by James Feher and reviewed by colleagues and provided by the University of Georgia's Global Textbook Project This lab manual provides an introduction to digital logic starting with simple gates and building up to state machines Students should have a solid understanding of algebra as well as a rudimentary understanding of basic electricity including voltage current resistance capacitance inductance and how they relate to direct current circuits [Introduction to Logic Circuits & Logic Design with Verilog](#) Brock J. LaMeres, 2023-10-19

This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i.e pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome [Introduction to Logic Circuits & Logic Design with VHDL](#) Brock J. LaMeres, 2016-09-15

This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i.e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a specific learning outcome that the learner should be able to do after its

completion The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1-7 followed by logic design Chapters 8-13 or a single accelerated course that uses the early chapters as reference material Fundamentals of Digital Electronics George B. Rutkowski, Jerome E. Oleksy, 1978

Design of Digital Systems Gregory L. Moss, Moss, 1997-06 **Digital Circuit Design Laboratory Manual** John Matthew Pavlina, Yelena Leonidovna Mukhortova, Akhan Almagambetov, 2020-05-23 Laboratory manual for Digital Circuit Design using Intel Quartus software Suitable for online and in person instruction Required for Embry Riddle Aeronautical University classes Includes the following laboratories 1 Introduction to Digital Circuits Lab 2 FPGA Design Workflow 3 Combinational Logic Circuits 4 Seven segment Display Logic 5 Combinational Logic using VHDL 6 Multiplexed Displays 7 Servo Motor Control 8 Accelerometer Interfacing 9 SPDT Switch Debouncing Multiplexers 10 Brushless DC Motor Control Covers the following topics in the appendices Basic Electrical Components HDL Design Using Intel Quartus Using Symbolic Blocks in Intel Quartus Multiplexing Pulse Width Modulation PWM Functional Simulation using ModelSim Has the following references in the appendices Development Board Pin Listings VHDL Source Code Listing Device Data *Digital Circuits Laboratory Manual* Farzin Asadi, 2023-08-30 Digital systems are an important part of modern life This book introduces the basic building blocks of digital systems and how these blocks can be used to design a digital system It can be used as a laboratory manual for courses such as Digital Logic and Digital Electronics All of the experiments in this book can be done in a simulation environment like Proteus or NI MultiSim or on the breadboard in a real laboratory environment Canadiana, 1988-09

Digital Logic Design John F. Passafiume, Michael Douglas, 1985 Digital Logic I.C. Lab Manual Gary E. Jones, 1978* Digital Logic Simulation with CPLD Programming Steve Waterman, 2003 This lab manual written around software and hardware developments of the past ten years focuses on the fundamentals of digital electronics and use of Max Plus II software by Altera Corporation Lab sequences start with digital gates and logic control circuits progress to MSI devices latches and flip flops and cover clock dependent circuits and the LPM_MACRO functions available in the software For individuals preparing to be computer technicians *Computer Design Aids for VLSI Circuits* P. Antognetti, Donald O. Pederson, Hugo De Man, 2013-11-11 The Nato Advanced Study Institute on Computer Design Aids for VLSI Circuits was held from July 21 to August 1 1980 at Sogesta Urbino Italy Sixty three carefully chosen professionals were invited to participate in this institute together with 12 lecturers and 7 assistants The 63 participants were selected from a group of almost 140 applicants Each had the background to learn effectively the set of computer IC design aids which were presented Each also had individual expertise in at least one of the topics of the Institute The Institute was designed to provide hands on type of experience rather than consisting of solely lecture and discussion Each morning detailed presentations were made concerning the critical algorithms that are used in the various types of computer IC design aids Each afternoon a lengthy

period was used to provide the participants with direct access to the computer programs In addition to using the programs the individual could if his expertise was sufficient make modifications of and extensions to the programs or establish limitations of these present aids The interest in this hands on activity was very high and many participants worked with the programs every free hour The editors would like to thank the Direction of SOGESTA for the excellent facilities 1r R Riccioni of the SOGESTA Computer Center and Mr 11 Vanzi of the University of Genova for enabling all the programs to run smoothly on the set date P Antognetti D O Pederson Urbino Summer 1980

Digital Logic Discrete Lab Manual Gary E.

Jones,1979* Scientific and Technical Aerospace Reports ,1994 **Popular Mechanics** ,1984-08 Popular Mechanics inspires instructs and influences readers to help them master the modern world Whether it's practical DIY home improvement tips gadgets and digital technology information on the newest cars or the latest breakthroughs in science PM is the ultimate guide to our high tech lifestyle *MSP430 Microcontroller Lab Manual* James Kretzschmar,Jeffrey Anderson,Steven F. Barrett,2023-05-23 This book is a practical reference for using Texas Instruments MSP430 microcontrollers It provides a series of hands on laboratory exercises The labs may be completed in a traditional laboratory setting or at home using the Digilent Analog Discovery 2 Test Instrument This book can be used as a reference for planning future projects using the MSP430 microcontroller The authors focus on applications of the main peripheral modules available on the MSP430 microcontroller CPU clock Basic Input Output Timer Analog to Digital Converter They also provide examples of how to develop Pulse Width Modulation signals and how to use Interrupts **American Book Publishing Record** ,2001

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