

# **Digital Logic** and **Microprocessor Design** with VHDL



**Enoch O. Hwang**

# Digital Logic And Microprocessor Design With Vhdl

**Uwe Meyer-Baese**



## Digital Logic And Microprocessor Design With Vhdl:

**Digital Logic And Microprocessor Design With Vhdl W/cd Hwang,**     Digital Logic and Microprocessor Design with VHDL Enoch O. Hwang, 2006 This book will teach students how to design digital logic circuits specifically combinational and sequential circuits Students will learn how to put these two types of circuits together to form dedicated and general purpose microprocessors This book is unique in that it combines the use of logic principles and the building of individual components to create data paths and control units and finally the building of real dedicated custom microprocessors and general purpose microprocessors After understanding the material in the book students will be able to design simple microprocessors and implement them in real hardware     *Digital Logic and Microprocessor Design with Interfacing* Enoch O. Hwang, 2016-12-05

DIGITAL LOGIC AND MICROPROCESSOR DESIGN WITH INTERFACING 2E provides a solid foundation for designing digital logic circuits This unique approach combines the use of logic principles and the building of individual components to create data paths and control units so readers can build dedicated custom microprocessors and general purpose microprocessors Readers design simple microprocessors from the ground up implement them in real hardware and interface them to actual devices Important Notice Media content referenced within the product description or the product text may not be available in the ebook version     Advanced FDTD Methods Wenhua Yu, 2011 Advanced FDTD Methods Parallelization Acceleration and Engineering Applications Contents Preface Chapter 1 Computational Electromagnetic Methods 1 1 FDTD METHOD 1 1 1 FDTD Update Equations 1 1 2 Stability Analysis 1 1 3 Boundary Conditions 1 2 METHOD OF MOMENTS 1 3 FINITE ELEMENT METHOD 1 3 1 Scalar Formulation 1 3 2 Vector Formulation 1 4 FINITE INTEGRATION TECHNIQUE References Chapter 2 FDTD Optimization and Acceleration 2 1 INTRODUCTION TO CPU ARCHITECTURE 2 2 SSE INSTRUCTION SET 2 3 CACHE OPTIMIZATION 2 4 TASK PARALLELIZATION AND BUNDLING 2 5 PREFETCH 2 6 READING OR WRITING COMBINATION 2 7 MATERIAL LOOP UP TABLE 2 8 NUMA OPTIMIZATION 2 9 IMPLEMENTATION OF VALU FDTD METHOD References Chapter 3 Parallel FDTD Method and Systems 3 1 PARALLEL FDTD METHOD 3 2 OPENMP FOR MULTICORE PROCESSORS 3 3 MPI TECHNIQUE 3 4 NETWORK CARD SWITCH AND CABLE References Chapter 4 Electromagnetic Simulation Techniques 4 1 MESH GENERATION TECHNIQUES 4 2 BASIC SIMULATION PROCEDURE 4 3 DIPOLE ANTENNA 4 4 VIVALDI ANTENNA SIMULATION 4 5 BANDED MICROWAVE CONNECTOR 4 6 PARALLEL LINES 4 7 TWO PORT ANTENNA 4 8 SLOT COUPLING 4 9 MICROWAVE FILTER 4 10 OPTIMIZATION AND PARAMETER SCAN 4 11 PERIODIC STRUCTURE SIMULATION 4 12 GROUND PENETRATING RADAR MODEL 4 13 MICROWAVE CONNECTOR References Chapter 5 EM Simulation Software Benchmarks 5 1 BASIC STEPS IN EM SIMULATION 5 1 1 HFSS 5 1 2 CST 5 1 3 FEKO 5 1 4 GEMS 5 2 HARDWARE PLATFORMS 5 3 PATCH ANTENNA 5 4 VIVALDI ANTENNA 5 5 SCATTERING OF DIELECTRIC SPHERE 5 6 CELL PHONE ANTENNA 5 7 ELECTROMAGNETIC BANDGAP STRUCTURE 5 8 STANDARD SAR TEST 5 9 WAVEGUIDE FILTER References Chapter 6 Large Multiscale Problem Solving 6 1 RADIO FREQUENCY

PROTECTION      **Digital Logic Circuits using VHDL** Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on digital electronics. The book provides basic concepts, procedures, and several relevant examples to help the readers to understand the analysis and design of various digital circuits. It also introduces hardware description language VHDL. The book teaches you the logic gates, logic families, Boolean algebra, simplification of logic functions, analysis and design of combinational circuits using SSI and MSI circuits, and analysis and design of the sequential circuits. This book provides in-depth information about multiplexers, demultiplexers, decoders, encoders, circuits for arithmetic operations, various types of flip-flops, counters, and registers. It also covers asynchronous sequential circuits, memories, and programmable logic devices.      *Fundamentals of Digital Logic and Microcomputer Design* M. Rafiquzzaman, 2005-06-06

*Fundamentals of Digital Logic and Microcomputer Design* has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition, the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor-based system design. Numerous examples are provided throughout the text. Coverage includes Digital circuits at the gate and flip-flop levels, Analysis and design of combinational and sequential circuits, Microcomputer organization, architecture, and programming concepts, Design of computer instruction sets, CPU memory, and I/O System design features associated with popular microprocessors from Intel and Motorola. Future plans in microprocessor development. An instructor's manual is available upon request. Additionally, the accompanying CD-ROM contains step-by-step procedures for installing and using Altera Quartus II software, MASM 6.11, 8086 and 68asm, and 68000 provides valuable simulation results via screen shots. *Fundamentals of Digital Logic and Microcomputer Design* is an essential reference that will provide you with the fundamental tools you need to design typical digital systems.      **Embedded**

**Microprocessor System Design using FPGAs** Uwe Meyer-Baese, 2025-05-29 This textbook for courses in Embedded Systems introduces students to necessary concepts through a hands-on approach. It gives a great introduction to FPGA-based microprocessor system design using state-of-the-art boards, tools, and microprocessors from Altera, Intel, and Xilinx. HDL-based designs, soft-core parameterized cores, Nios II, and MicroBlaze, and ARM Cortex A9 design are discussed, compared, and explored using many hands-on designs and projects. Custom IP for HDMI coder, Floating point operations, and FFT bit swap are developed, implemented, tested, and speed-up is measured. New additions in the second edition include bottom-up and top-down FPGA-based Linux OS system designs for Altera, Intel, and Xilinx boards and application development running on the OS using modern popular programming languages Python, Java, and JavaScript. HTML CSSs. Downloadable files include all design examples such as basic processor synthesizable code for Xilinx and Altera tools for PicoBlaze, MicroBlaze, Nios II, and ARMv7 architectures in VHDL and Verilog code, as well as the custom IP projects. For the three new OS-enabled programming

languages a substantial number of examples ranging from basic math and networking to image processing and video animations are provided Each Chapter has a substantial number of short quiz questions exercises and challenging projects

**Digital Design (VHDL)** Peter J. Ashenden, 2007-10-24 Digital Design An Embedded Systems Approach Using VHDL provides a foundation in digital design for students in computer engineering electrical engineering and computer science courses It takes an up to date and modern approach of presenting digital logic design as an activity in a larger systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized VHDL examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of VHDL examples to demonstrate HDL hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments Includes worked examples throughout to enhance the reader's understanding and retention of the material Companion Web site includes links to tools for FPGA design from Synplicity Mentor Graphics and Xilinx VHDL source code for all the examples in the book lecture slides laboratory projects and solutions to exercises

**Uncertainty and Imprecision in Decision Making and Decision Support:**

**Cross-Fertilization, New Models and Applications** Krassimir T. Atanassov, Janusz Kacprzyk, Andrzej Kałuszko, Maciej Krawczak, Jan Owsinski, Sotir Sotirov, Evdokia Sotirova, Eulalia Szmidt, Sławomir Zadrozny, 2017-10-06 This book presents selected papers from two important conferences held on October 12-14 2016 in Warsaw Poland the Fourteenth National Conference of Operational and Systems Research BOS 2016 one of the premiere conferences in the field of operational and systems research not only in Poland but also at the European level and the Fifteenth International Workshop on Intuitionistic Fuzzy Sets and General Nets IWIFSGN 2016 one of the foremost conferences on fuzzy logic notably addressing extensions of the traditional fuzzy sets as well as the Generalized Nets GNs a powerful extension of the traditional Petri net paradigm The scope of the BOS conferences includes all types of problems related to systems modeling systems analysis broadly perceived operational research optimization decision making and decision support to name but a few In all these areas virtually all models used have to take into account not only uncertainty in its traditional sense but also imprecision of information That is in addition to traditional probabilistic and statistical tools and techniques the use of methods based on fuzzy sets can also be sensible Even more so employing certain extensions of the classic concept of a fuzzy set can be very useful Applying intuitionistic fuzzy sets which are at the core of the IWIFSGN conferences is a good example Both conferences BOS 2016 and IWIFSGN 2016 offered ideal venues for the exchange of ideas cross fertilization and mutual inspiration

**FUNDAMENTALS OF DIGITAL LOGIC AND MICROCOMPUTER DESIGN, 5TH ED (With CD )** M. Rafiquzzaman (with Cd), 2009-09-01 Market\_Desc Undergraduate courses on digital logic design computer architecture and microprocessors Graduate students and practicing microprocessor system designers in industry Special Features While most texts either focus on computer design or digital logic and digital systems this book includes both areas making it a unique addition to existing literature The author has an extensive background in computers and has published numerous books on the subject He is undoubtedly one of the leading authorities in this field This book covers simple topics such as number system and Boolean algebra to advanced topics such as assembly language programming and microprocessor based system design The accompanying CD contains a step by step procedure for installing and using Altera Quartus II software for synthesizing Verilog and VHDL descriptions Screen shots of the waveforms and tabular forms illustrating the simulation results are also provided in the CD The CD also contains a step by step procedure for installing and using MASM 6 11 8086 and 68asm sim 68000 Screen shots verifying correct operations of several assembly language programs via simulation using test data are also provided in the CD About The Book This book covers all basic concepts of computer engineering and science from digital logic circuits to the design of a complete microcomputer system in a methodical and basic manner Its intention is to present a clear understanding of the principles and basic tools required to design typical digital systems such as microcomputers The book covers the latest version of Altera software called Quartus II It provides a simplified introduction to VHDL along with a step by step procedure with tutorials on a CD It is ideal for an introductory course in VHDL containing digital logic and microprocessors along with both VHDL and Verilog The material in the text is divided into three sections Fundamentals of digital logic circuits and design Microprocessor microcomputer design Overview of 16 32 and 64 bit microprocessors manufactured by Intel and Motorola *Introduction to Logic Circuits & Logic Design with VHDL* Brock J. LaMer, 2023-10-24 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a specific learning outcome that the learner should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1 7 followed by logic design Chapters 8 14 or a single

accelerated course that uses the early chapters as reference material      *Introduction to Logic Circuits & Logic Design with Verilog* Brock J. LaMeres, 2017-04-17 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i.e. pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning Goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome      *DIGITAL ELECTRONICS, COMPUTER ARCHITECTURE AND MICROPROCESSOR DESIGN PRINCIPLES: WITH REAL LIFE PRACTICAL APPLICATION IN COMPUTING, NETWORKING, MINING, REMOTE SENSING, DATABASE AND IMAGERY* Jagdish Krishanlal Arora, Table of Contents CHAPTER 1 MICROPROCESSOR CHAPTER 2 SILICON WAFERS CHIPS CHAPTER 3 TRANSISTORS CHAPTER 4 LOGIC GATES CHAPTER 5 BOOLEAN ALGEBRA AND STORING NUMBERS CHAPTER 6 BINARY CONVERSION OF TEXT AUDIO IMAGE AND VIDEO CHAPTER 7 DATA COMPRESSION CHAPTER 8 REGISTERS CHAPTER 9 THE CONTROL UNIT CHAPTER 10 ARITHMETIC LOGIC UNIT ALU CHAPTER 11 DATA PATHS AND MULTIPLEXERS CHAPTER 12 BIOS Basic Input Output System CHAPTER 13 ASSEMBLY LANGUAGE CHAPTER 14 HARD DISK CHAPTER 15 RAM AND ROM CHAPTER 16 DIFFERENT TYPES OF MICROPROCESSORS CHAPTER 17 ASIC Application Specific Integrated Circuit CHAPTER 18 FPGA Field Programmable Gate Array CHAPTER 19 PRISM Parallel Reduced Instruction Set Multiprocessor CHAPTER 20 COMPUTER MOTHERBOARDS CHAPTER 21 WIRELESS COMMUNICATION CHAPTER 22 KEYBOARD AND MOUSE CHAPTER 23 ROUTER AND SWITCHES CHAPTER 24 OPERATING SYSTEM CHAPTER 25 Project DESIGNING A 4 BIT MICROPROCESSOR CHAPTER 26 ROBOTICS CHAPTER 27 ARTIFICIAL INTELLIGENCE CHAPTER 28 NETWORKING CHAPTER 29 CLOUD COMPUTING AND CLOUD STORAGE CHAPTER 30 DATABASES CHAPTER 31 BLOCK CHAIN CRYPTOCURRENCY AND MINING CHAPTER 32 REMOTE SENSING      *Embedded Systems* Kiyofumi Tanaka, 2012-03-02 Nowadays embedded systems the computer systems that are embedded in various kinds of devices and play an important role of specific control functions have permitted various aspects of industry Therefore we can hardly discuss our life and society from now onwards without referring to embedded systems For wide ranging embedded systems to continue their growth a number of high quality fundamental and applied researches are indispensable This book contains 19 excellent chapters and addresses a wide spectrum of research topics on embedded systems including basic researches theoretical studies and practical work Embedded systems can be made only after fusing

miscellaneous technologies together Various technologies condensed in this book will be helpful to researchers and engineers around the world

**Digital Logic** M. Rafiquzzaman, Steven A. McNinch, 2019-09-11 Digital Logic with an Introduction to Verilog and FPGA Based Design provides basic knowledge of field programmable gate array FPGA design and implementation using Verilog a hardware description language HDL commonly used in the design and verification of digital circuits Emphasizing fundamental principles this student friendly textbook is an ideal resource for introductory digital logic courses Chapters offer clear explanations of key concepts and step by step procedures that illustrate the real world application of FPGA based design Designed for beginning students familiar with DC circuits and the C programming language the text begins by describing of basic terminologies and essential concepts of digital integrated circuits using transistors Subsequent chapters cover device level and logic level design in detail including combinational and sequential circuits used in the design of microcontrollers and microprocessors Topics include Boolean algebra and functions analysis and design of sequential circuits using logic gates FPGA based implementation using CAD software tools and combinational logic design using various HDLs with focus on Verilog

*Digital Design and Fabrication* Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user s point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design

*Digital Systems Design with FPGAs and CPLDs* Ian Grout, 2011-04-08 Digital Systems Design with FPGAs and CPLDs explains how to design and develop digital electronic systems using programmable logic devices PLDs Totally practical in nature the book features numerous quantify when known case study designs using a variety of Field Programmable Gate Array FPGA and Complex Programmable Logic Devices CPLD for a range of applications from control and instrumentation to semiconductor automatic test equipment Key features include Case studies that provide a walk through of the design process highlighting the trade offs involved Discussion of real world issues such as choice of device pin out power supply power supply decoupling signal integrity for embedding FPGAs within a PCB based design With this book engineers will be able to Use PLD technology to develop digital

and mixed signal electronic systems Develop PLD based designs using both schematic capture and VHDL synthesis techniques Interface a PLD to digital and mixed signal systems Undertake complete design exercises from design concept through to the build and test of PLD based electronic hardware This book will be ideal for electronic and computer engineering students taking a practical or Lab based course on digital systems development using PLDs and for engineers in industry looking for concrete advice on developing a digital system using a FPGA or CPLD as its core Case studies that provide a walk through of the design process highlighting the trade offs involved Discussion of real world issues such as choice of device pin out power supply power supply decoupling signal integrity for embedding FPGAs within a PCB based design

**Digital Design and Computer Architecture** David Harris, Sarah Harris, 2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material

The Computer Engineering Handbook Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own

**Digital Signal Processing with Field Programmable Gate Arrays** Uwe Meyer-Baese, 2007-11-14 A practical and fascinating book on a topic at the forefront of communications technology Field Programmable Gate Arrays FPGAs are on the verge of revolutionizing digital signal processing Novel FPGA families are replacing ASICs and PDSs for front end digital signal processing algorithms at an accelerating rate The efficient implementation of these algorithms is the main goal of this book It starts with an overview of today s FPGA technology devices and tools for designing state of the art DSP systems Each of the book s chapter contains exercises The VERILOG source code and a glossary are given in the appendices

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