



Figure P9.6

SOLUTION:

The total charge at node C_L before charge sharing is

$$Q_L = V_{in} \cdot C_L = 5C_L$$

After charge sharing, suppose the node voltage is V_{final} , then

$$Q_L = 5C_L = V_{final} \cdot C_L + V_{final} \cdot C_P$$

To prevent the inverter from switching,

$$V_{final} = \frac{5C_L}{C_L + C_P} > 3$$

The maximum ratio is:

$$\frac{C_P}{C_L} < \frac{2}{3}$$

9.7 Consider the following CMOS logic circuit which is a simple domino circuit. Node X is connected to a CMOS inverter so that the output of the inverter can be directly fed to the next stage of the domino circuit.

- (a) Explain how the voltage level at node X, after it is precharged to 5 V, can be affected by the charge sharing between node X and node Y if their node capacitances are the same. Express the final voltage at node X in terms of the initial voltage at node Y when the charge sharing is completed following the full precharge operation when the gate terminal of M2 transistor is fixed at 0 V.

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Ming-Bo Lin



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Solutions Manual for Digital Integrated Circuits Ayers John E, 2003-09 **Solutions Manual to Accompany Analysis and Design of Digital Integrated Circuits** David A. Hodges, Yu Chen, Horace G. Jackson, 1983 *Solution Manual to Accompany CMOS Digital Integrated Circuits : Analysis and Design, Second Edition* Sung-Mo Kang, Yusuf Leblebici, 1999 *Solutions Manual Digital Integrated Circuits* CRC Press, 2009-07-10 Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer simulations The book's website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor's manual *Electronic Devices and Circuit Fundamentals, Solution Manual* Dale R. Patrick, Stephen W. Fardo, Ray E. Richardson, Vigyan (Vigs) Chandra, 2023-05-26 *Devices and Circuit Fundamentals* is Chapter Outline Learning Objectives Key Terms Figure List Chapter Summary Formulas Answers to Examples Self Exams Glossary of Terms defined **Digital Integrated Circuits** DeMassa, 1996-02-01 **Solutions Manual** Kang, 1995-08-01

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Digital Integrated Circuits Evgeni Perelroyzen, 2018-10-03 A current trend in digital design the integration of the MATLAB components Simulink and Stateflow for model building simulations system testing and fault detection allows for better control over the design flow process and ultimately for better system results *Digital Integrated Circuits Design for Test Using Simulink and Stateflow* illustrates the construction of Simulink models for digital project test benches in certain design for test fields The first two chapters of the book describe the major tools used for design for test The author explains the process of Simulink model building presents the main library blocks of Simulink and examines the development of finite state machine modeling using Stateflow diagrams Subsequent chapters provide examples of Simulink modeling and simulation for the latest design for test fields including combinational and sequential circuits controllability and observability deterministic algorithms digital circuit dynamics timing verification built-in self test BIST architecture scan cell operations and functional and diagnostic testing The book also discusses the automatic test pattern generation ATPG process the logical determinant theory and joint test action group JTAG interface models *Digital Integrated Circuits* explores the possibilities of MATLAB's tools in the development of application specific integrated circuit ASIC design systems The book shows how to incorporate Simulink and Stateflow into the process of modern digital design **Engineering Education**, 1989 *Introduction to VLSI Systems* Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature

sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding

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Fundamentals of Solid-state Electronics Chih-Tang Sah, 1996 This Solution Manual a companion volume of the book Fundamentals of Solid State Electronics provides the solutions to selected problems listed in the book Most of the solutions are for the selected problems that had been assigned to the engineering undergraduate students who were taking an introductory device core course using this book This Solution Manual also contains an extensive appendix which illustrates the application of the fundamentals to solutions of state of the art transistor reliability problems which have been taught to advanced undergraduate and graduate students

Subject Guide to Books in Print, 1993

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