



Figure P9.6

SOLUTION:

The total charge at node C_L before charge sharing is

$$Q_L = V_{in} \cdot C_L = 5C_L$$

After charge sharing, suppose the node voltage is V_{final} , then

$$Q_L = 5C_L = V_{final} \cdot C_L + V_{final} \cdot C_P$$

To prevent the inverter from switching,

$$V_{final} = \frac{5C_L}{C_L + C_P} > 3$$

The maximum ratio is:

$$\frac{C_P}{C_L} < \frac{2}{3}$$

9.7 Consider the following CMOS logic circuit which is a simple domino circuit. Node X is connected to a CMOS inverter so that the output of the inverter can be directly fed to the next stage of the domino circuit.

- (a) Explain how the voltage level at node X, after it is precharged to 5 V, can be affected by the charge sharing between node X and node Y if their node capacitances are the same. Express the final voltage at node X in terms of the initial voltage at node Y when the charge sharing is completed following the full precharge operation when the gate terminal of M2 transistor is fixed at 0 V.

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Yijin Wang



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